Formal Methods for Software Development Modeling Concurrency

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Concurrent Systems – The Big Picture

Concurrency: different processes trying not to run into each others' way

Main problem of concurrency: sharing computational resources

http://www.youtube.com/watch?v=JgMB6nEv7K0 http://www.youtube.com/watch?v=G8eqymwUFi8

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processes = vehicles, shared resource = crossing, and a (data) race in progress.

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 $\mathsf{processes} = \mathsf{vehicles}, \mathsf{shared} \mathsf{ resource} = \mathsf{crossing}, \mathsf{and} \mathsf{ a} \mathsf{ (data)} \mathsf{ race} \mathsf{ in} \mathsf{ progress}.$

Solutions to this must be carefully designed and verified, otherwise...

Concurrent Systems – The Big Picture



exhibit flaws in	software systems
------------------	------------------

exhibit design flaws in

software systems

exhibit design flaws in concurrent and distributed software systems

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Focus of this lecture:

Modeling and analyzing concurrent systems

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Focus of this lecture:

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Focus of next lecture:

Modeling and analyzing distributed systems

Problems:

hard to predict, hard to form faithful intuition

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lack of time

exhaustive testing exhausts the testers long before it exhausts behavior of the system...

Mission of $\operatorname{Spin}\text{-style}$ Model Checking

Offer an efficient methodology to

improve the design

exhibit defects

of concurrent and distributed systems

Activities in $\operatorname{Spin}\text{-style}$ Model Checking

- 1. model (critical aspects of) concurrent/distributed system with PROMELA
- 2. state crucial properties with assertions, temporal logic,
- 3. use SPIN to check all possible runs of the model
- 4. analyze result, possibly re-work 1. and 2.

Activities in $\operatorname{Spin}\text{-style}$ Model Checking

- 1. model (critical aspects of) concurrent/distributed system with PROMELA
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- **3.** use SPIN to check all possible runs of the model
- 4. analyze result, possibly re-work 1. and 2.

Separate concerns of model vs. property! Check the property you want the model to have, not the one it happens to have.

expressiveness

Model must be expressive enough to 'embrace' defects the real system could have

simplicity

Model must be simple enough to be 'model checkable', theoretically and practically

Modeling Concurrent Systems in Promela

In the SPIN approach, the cornerstone of modeling concurrent/distributed systems are

PROMELA processes.

Can be instantiated *implicitly* using 'active'.

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```
Can be instantiated explicitly with key word 'init'.
init {
    printf("Hello_world\n")
}
```

init mostly used to start other processes with run statement.

Starting Processes

```
Processes can be started explicitly using run.
proctype P() {
   byte x;
   ...
}
init {
   run P();
   run P()
}
```

Each run operator starts copy of process (with copy of local variables).

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(PROMELA's run corresponds to JAVA's start, not to JAVA's run)

Atomic Start of Multiple Processes

```
By convention, run operators enclosed in atomic block
proctype P() {
  byte x;
  . . .
}
init {
  atomic {
    run P();
    run P()
  }
```

}

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Effect: processes only start executing once all are created

```
(More on atomic later)
```

Joining Processes

```
joining: waiting for all other processes to finish
byte result;
proctype P() {
   . . .
}
init {
  atomic {
    run P();
    run P()
  }
  (_nr_pr == 1); /* blocked until _nr_pr == 1 */
  printf("result_=%d", result)
}
```

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    run P();
    run P()
  }
  (_nr_pr == 1); /* blocked until _nr_pr == 1 */
  printf("result_=%d", result)
}
```

_nr_pr built-in variable holding number of running processes _nr_pr == 1 only 'this' process (init) is running Processes may have formal parameters, instantiated by run:

```
proctype P(byte id; byte incr) {
    ...
}
init {
    run P(7, 10);
    run P(8, 15)
}
```

Active (Sets of) Processes

init can be made implicit by using the active modifier:

```
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    ...
}
```

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```
active proctype P() {
    ...
}
Implicit init will run one copy of P
active [n] proctype P() {
    ...
}
Implicit init will run n copies of P
```

Variables declared outside of the processes are global to all processes.

Variables declared inside a process are local to that processes. byte n;

```
proctype P(byte id; byte incr) {
    byte t;
    ...
}
n is global
t is local
```

Pragmatics of modeling with global data:

Shared memory of concurrent systems often modeled by global variables of numeric (or/array) type.

Status of shared resources (printer, traffic light, ...) often modeled by global variables of Boolean or enumeration type (bool/mtype).

Communication mediums of distributed systems often modeled by global variables of channel type (chan). (next lecture)

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("ProcuP,unu=u%d\n", n)
}
```

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("Proc_UP, un_U=_U%d\n", n)
}
active proctype Q() {
  n = 2;
  printf("Proc_UQ, un_U=_U%d\n", n)
}
```

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("ProcuP,unu=u%d\n", n)
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  n = 2;
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```

How many outputs possible?

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("Proc_UP,_Un_U=_U%d\n", n)
}
active proctype Q() {
  n = 2;
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}
```

How many outputs possible?

Different processes can interfere on global data

- 1. interleave0.pml SPIN simulation, SPINSPIDER automata + transition system
- interleave1.pml SPIN simulation, adding assertion, fine-grained execution model, model checking
- 3. interleave5.pml SPIN simulation, SPIN model checking, trail inspection

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Most synchronization primitives (test & set, compare & swap, semaphores, ...) can be modeled w. *executability and atomicity*.

Each statement has the notion of executability. Executability of basic statements:

statement type	executable
assignment	always
assertion	always
print statement	always
expression statement	
send/receive statement	

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send/receive statement	(next lecture)

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if resp. do statement is executable $\label{eq:final} \begin{array}{c} \text{iff} \\ \\ \text{any of its alternatives}^1 \text{ is executable} \end{array}$

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An alternative is executable iff its guard (the first statement) is executable (Recall: in alternatives, "->" syntactic sugar for ";")

(Inspect end.pml)

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Definition (Blocking)

A statement blocks iff it is *not* executable.

A process blocks iff its location counter points to a blocking statement.

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Executability, resp. blocking are the key to ${\rm PROMELA}\xspace$ -style modeling of solutions to synchronization problems.

Definition (Deadlock (simplified))

Let *CRP* be the set of *Currently Running Processes*. A deadlock is a point in the execution where

•
$$CRP \neq \emptyset$$

▶ all $p \in CRP$ are blocking

(Model check end.pml)

Definition (End Location)

End locations of a process P are:

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- P's textual end
- each location marked with an end label: "endxxx:"

Definition (Deadlock (full version))

Let CRP be the set of Currently Running Processes.

Let $NEL \subseteq CRP$ be the set of (currently running) processes which are Not at a valid End Location.

A deadlock is a point in the execution where

- NEL $\neq \emptyset$
- ▶ all $p \in NEL$ are blocking

Deadlock Detection

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- 'invalid end state' error (in verification mode)
- 'timeout' in simulation mode

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(Fix end.pml)

Limit the possibility of sequences being interrupted by other processes weakly atomic sequence Can *only* be interrupted when a statement blocks.

strongly atomic sequence

Cannot be interrupted at all.

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Cannot be interrupted at all.

Defined in PROMELA by d_step{list_of_statements}

Deterministic Sequences

 d_step :

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Deterministic Sequences

 d_step :

- strongly atomic
- deterministic (like a single step)
- choices resolved in fixed way (always take the first possible option)
 ⇒ avoid choices in d_step
- it is an error if any statement within d_step, other than the first one (called 'guard'), blocks

```
d_step {
	stmt1; ← guard
	stmt2;
	stmt3
}
```

If stmt1 blocks, d_step is not entered, and blocks as a whole.

It is an error if stmt2 or stmt3 block.

(Weakly) Atomic Sequences

atomic:

```
weakly atomic
```

can be non-deterministic

```
atomic {
    stmt1; ← guard
    stmt2;
    stmt3
}
```

If guard blocks, atomic is not entered, and blocks as a whole.

Once **atomic** is entered, control is kept until a statement blocks, and only in this case passed to another process.

Archetypal problem of concurrent systems

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Absence of Deadlock When all processes try to enter their critical sections, one of them must succeed.

Absence of (individual) Starvation When *any* process tries to enter its critical section, then *that* process must eventually succeed.

Critical Section Pattern

For demonstration and simplicity: noncritical and critical sections only **printf** statements here.

```
active proctype P() {
  do :: printf("P_noncritical_actions\n");
         /* begin critical section */
         printf("P_{\sqcup}uses_{\sqcup}shared_{\sqcup}resourses \n")
         /* end critical section */
  od
}
active proctype Q() {
  do :: printf("Q_noncritical_actions\n");
         /* begin critical section */
         printf("Q_{\sqcup}uses_{\sqcup}shared_{\sqcup}resourses \n")
         /* end critical section */
  od
}
```

No Mutual Exclusion Yet

```
More infrastructure to achieve ME.
Adding two Boolean flags:
```

```
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("P_non-critical_actions\n");
        P in CS = true:
        /* begin critical section */
        printf("P_uses_shared_resourses\n");
        /* end critical section */
        P in CS = false
 od
}
active proctype Q() {
  ...correspondingly...
```

}

Show Mutual Exclusion VIOLATION with SPIN

```
adding assertions
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
         P_{in}CS = true;
         /* begin critical section */
         printf("P_{||}uses_{||}shared_{||}resourses \n");
         assert(!Q_in_CS);
         /* end critical section */
        P_in_CS = false
  od
}
active proctype Q() {
    .....assert(!P_in_CS);.....
}
```

Mutual Exclusion by Busy Waiting

```
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("P_non-critical_actions\n");
        P_{in}CS = true;
        do :: !Q_in_CS -> break
            :: else -> skip
        od;
        /* begin critical section */
        printf("P_{||}uses_{||}shared_{||}resourses \n");
        assert(!Q_in_CS);
        /* end critical section */
        P_{in}CS = false
  od
}
```

active proctype Q() { ...correspondingly... }

Instead of Busy Waiting, process should

- 1. yield control when "waiting",
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What can we do instead?

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What can we do instead?

We use expression statement !Q_in_CS to let process P block where it should not proceed!

Mutual Exclusion by Blocking

```
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
         P_{in}CS = true;
         !Q_in_CS:
         /* begin critical section */
         printf("P_{\cup}uses_{\cup}shared_{\cup}resourses \n");
         assert(!Q_in_CS);
         /* end critical section */
         P in CS = false
  od
}
active proctype Q() {
  ...correspondingly...
}
```

Verify Mutual Exclusion of this

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can make pan ignore the deadlock: ./pan -E

Verify with SPIN

```
\begin{array}{l} {\rm SPIN} \mbox{ error (invalid end state)} \\ \Rightarrow \mbox{ deadlock} \end{array}
```

can make pan ignore the deadlock: ./pan -E

SPIN still reports assertion violation(!)

mutual exclusion (ME) cannot be shown by SPIN

- mutual exclusion (ME) cannot be shown by SPIN
- P/Q_in_CS sufficient for achieving ME

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Need more infrastructure.

Ghost variables: variables for verification, not for modeling

Show Mutual Exclusion with Ghost Variable

```
int critical = 0;
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
         P_{in}CS = true;
         !Q in CS:
         /* begin critical section */
         critical++:
         printf("P_{||}uses_{||}shared_{||}resourses \n");
         assert(critical < 2);</pre>
         critical--:
         /* end critical section */
         P_in_CS = false
  od
}
active proctype Q() {
  ...correspondingly...
3
```

Verify Mutual Exclusion of this

SPIN (./pan -E) shows no assertion is violated \Rightarrow mutual exclusion is verified

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SPIN (./pan -E) shows no assertion is violated \Rightarrow mutual exclusion is verified

Still SPIN (without -E) reports (invalid end state) \Rightarrow deadlock

Deadlock Hunting

Invalid End State:

- A process does not finish at its end
- OK if it is not crucial to continue add end lables (see end.pml)
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Address Deadlock with SPIN:

- Verify to produce a failing run trail
- Simulate to see how the processes get to the interlock
- ► Fix the model (not using the end labels nor -E option)

Atomicity against Deadlocks

solution:

checking and setting the flag in one atomic step

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(demonstrate that in csGhost.pml)

Atomicity against Deadlocks

solution:

checking and setting the flag in one atomic step

(demonstrate that in csGhost.pml)

```
atomic {
    !Q_in_CS;
    P_in_CS = true
}
```

Verification artifacts:

Ghost variables (add variables for verification)

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- Temporal logic (later in the course)

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 Readers exclude writers, but not other readers.
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 - ... and many more

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- d_step excludes any nondeterminism!

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Using atomic and d_step too heavily, for too large blocks, can result in well-behaved models, while modeling the wrong system.