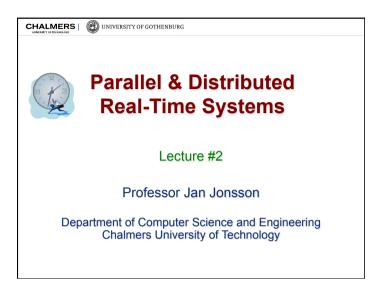
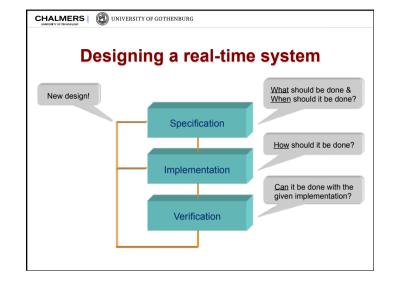
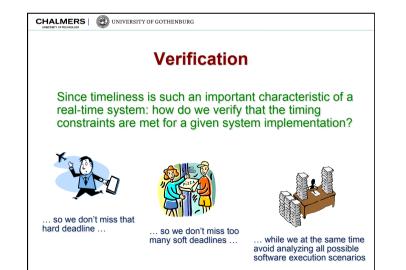
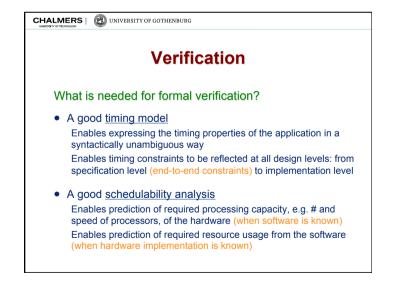
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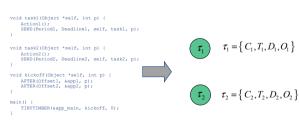
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#### (B) UNIVERSITY OF GOTHENBURG CHALMERS | WIVERSITY OF GOTHENBURG CHALMERS Verification Verification What sources of uncertainty exist in formal verification? How do we simplify formal verification? • Non-determinism in tasks' WCET (undisturbed execution) Concurrent and reactive programming paradigm - Suitable schedulable unit of concurrency (task, thread, ...) Input data and internal state controls execution paths - Memory access patterns control delays in processor - Language constructs for expressing application constraints architecture (pipelines and cache memories) for schedulable unit (priorities, delays, ...) - WCET must be derivable for schedulable unit (special caution Non-determinism in tasks' execution interference with usage of dynamic language constructs) (pseudo-parallel execution) Deterministic task execution - Run-time execution model controls interference pattern - Time tables or static/dynamic task priorities Conflicts in tasks' demands for shared resources Preemptive task execution - (Pseudo-)parallel task execution may give rise to uncontrolled - Run-time protocols for access to shared resources (dynamic blocking of shared hardware and software resources priority adjustment and non-preemptable code sections)

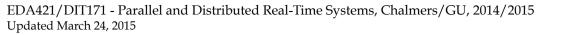
## CHALMERS (E) UNIVERSITY OF GOTHENBURG Verification How do we perform schedulability analysis? • Introduce abstract models of system components: - Task model (computation requirements, timing constraints) - Processor model (resource capacities) - Run-time model (task states, dispatching) Predict whether task executions will meet constraints - Use abstract system models

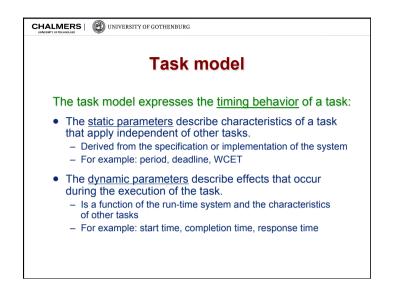
- Make sure that computation requirements never exceed resource capacities
- Generate (partly or completely) run-time schedule resulting from task executions and detect worst-case scenarios

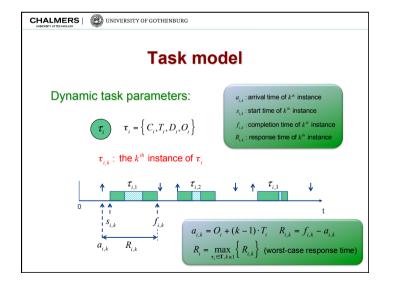
# CHALMERS | ( UNIVERSITY OF GOTHENBURG Task model Implementation Abstract model

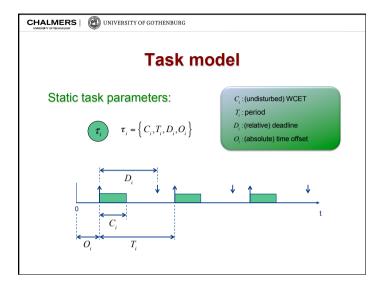


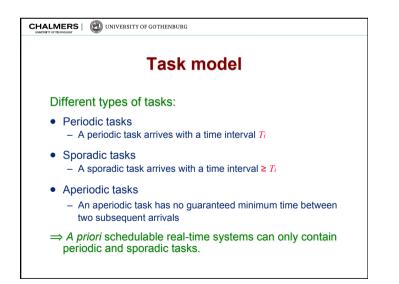
### 3





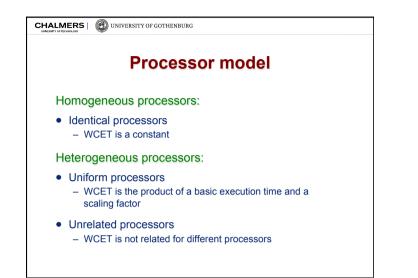


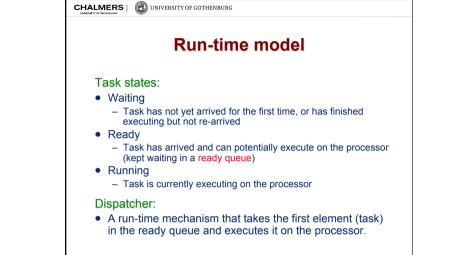


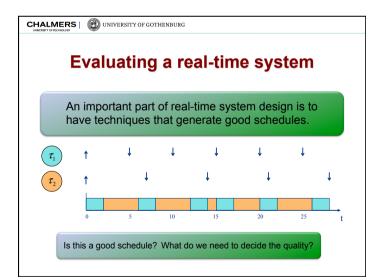


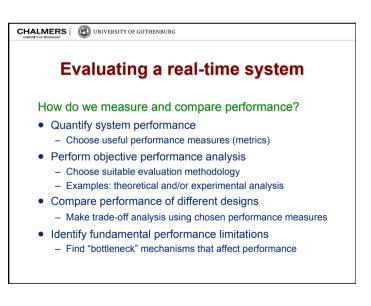
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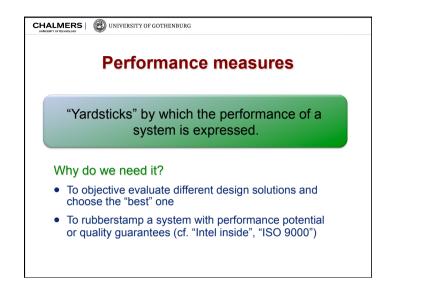


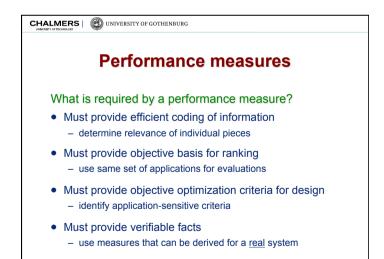


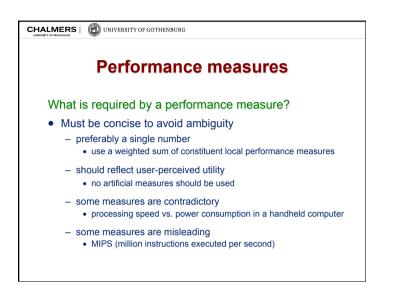


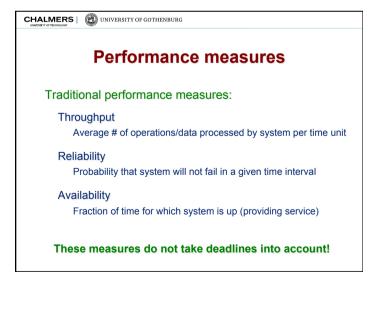
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