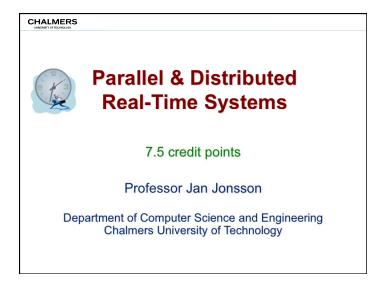
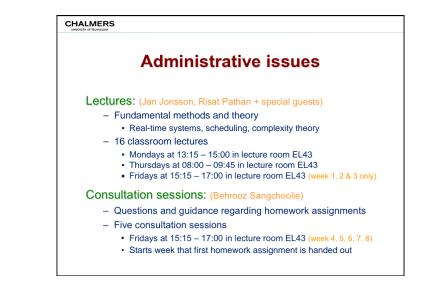
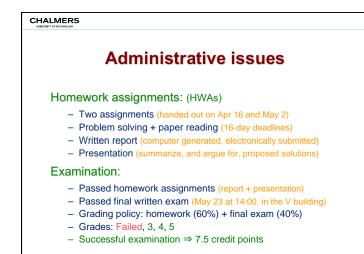
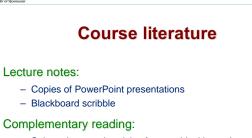
CHALMERS









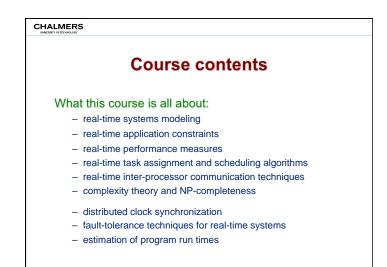
- Selected research articles from archival journals and conference proceedings
- Selected chapters from C. M. Krishna and K. G. Shin, "Real-Time Systems", McGraw-Hill, 1997 (+ errata list!)

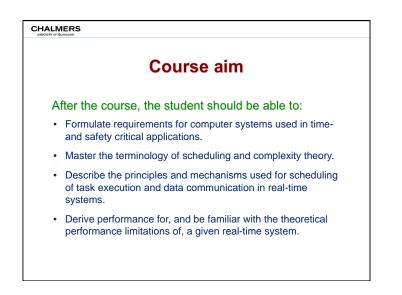
2

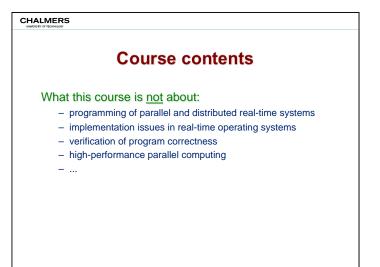
EDA421/DIT171 - Parallel and Distributed Real-Time Systems, Chalmers/GU, 2011/2012 Updated March 10, 2012

Characteristic Resources Consultation sessions: - Fridays at 15:15 – 17:00 in room EL43 Student portal: - Administration of HWAs (form groups, submit documents, etc) - Results from the grading of HWAs and written exam Information board: http://www.cse.chalmers.se/edu/course/EDA421

Lecture notes will be available on the information board no later than 48 hours before the corresponding lecture

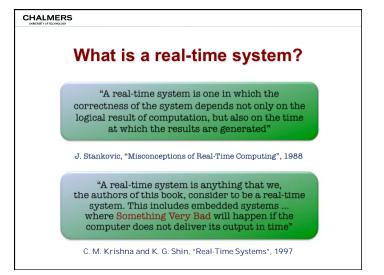


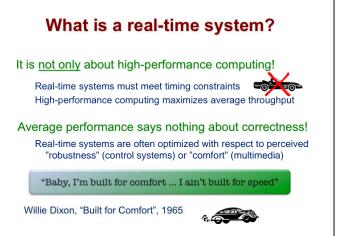


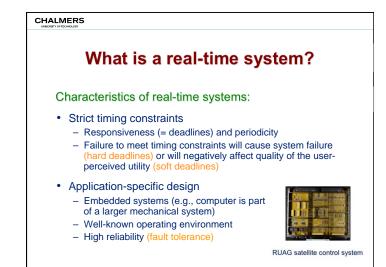


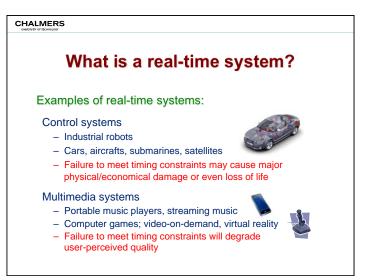
What is a real-time	syste

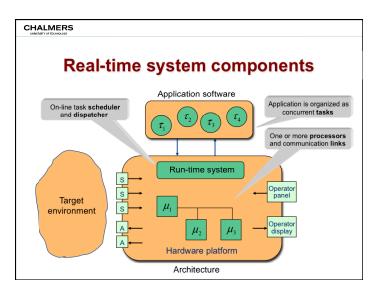
CHALMERS

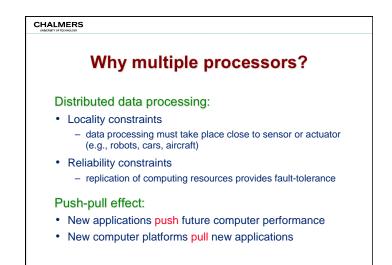


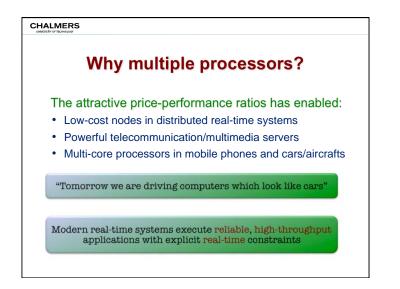












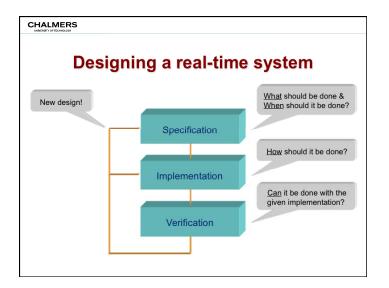


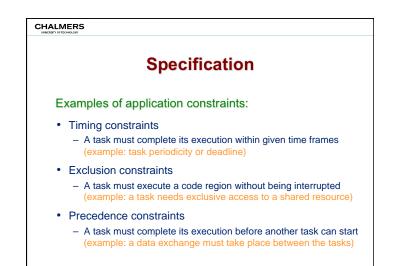
Why multiple processors?

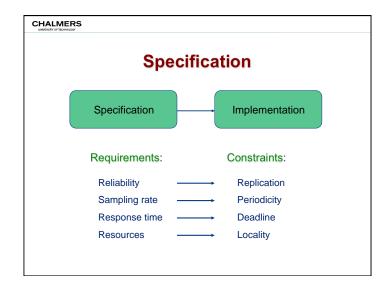
New intriguing possibilities:

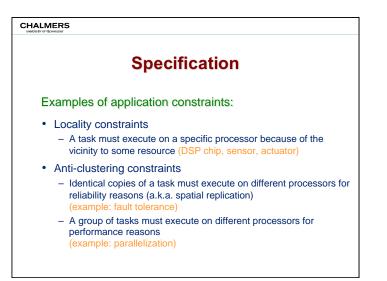
Lecture #1

- · High throughput
 - parallel execution of tasks
 - parallelization of algorithms (e.g., graphic algorithms)
- · High schedulability
 - advanced scheduling algorithms (e.g., bidding, parallel B&B)
 - advanced dispatchers (e.g., affinity-based)
- · High reliability
 - advanced fault-detection techniques (for high coverage)
 - massive redundancy (in time or space)









EDA421/DIT171 - Parallel and Distributed Real-Time Systems, Chalmers/GU, 2011/2012 Updated March 10, 2012

CHALMERS CHALMERS **Specification** Examples of application constraints: Clustering constraints - A group of tasks must execute on the same processor for functional reasons (example: only one processor is used in low-power mode) - A group of tasks must execute on the same processor for performance reasons (example: intensive communication within the group)

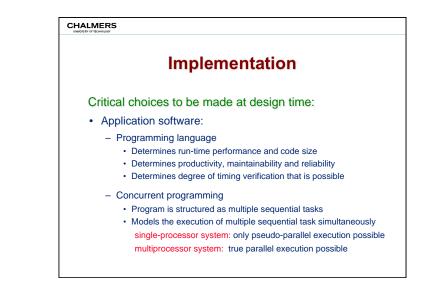
- A group of tasks must execute on the same processor for security reasons (example: risk for eavesdropping of network bus)

Where do the timing constraints come from? Laws of nature - Bodies in motion: arm movements in a robotic system - Inertia of the eye: minimal frame rate in film Mathematical theory - Control theory: recommended sampling rate Component limitations

- Sensors and actuators: minimal time between operations

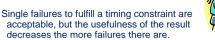
Specification

- Artificial derivation
 - Observable events: certain (global) timing constraints are given, but individual (local) timing constraints are needed



If the system fails to fulfill a timing constraint, the computational results is useless.

Soft constraints:



Statistical guarantees often suffice for these systems!

How critical are the constraints?

Hard constraints:

CHALMERS

Specification

Correctness must be verified before system is put in mission!





EDA421/DIT171 - Parallel and Distributed Real-Time Systems, Chalmers/GU, 2011/2012 Updated March 10, 2012

CHALMERS

CHALMERS Implementation Implementation Critical choices to be made at design time: Critical choices to be made at design time: · Hardware architecture: • Run-time system: - Single or multiprocessor architecture - System services · Determines degree of true parallelism that can be exploited • Operating system (real-time kernel with system calls) - Microprocessor family • Stand-alone system (linked library with subroutine calls) • Determines run-time performance and code size • RISC processor (pipelines, caches, support for multiprocessors) · Determines cost, flexibility and portability • Micro-controller (no, or very simple, pipelines/caches) · Determines cost and run-time performance • Determines difficulty in worst-case execution time (WCET) analysis - Communication network technology and topology · Determines cost, performance and reliability

- Task and message dispatching model · Time vs. priority driven dispatching Preemptive vs. non-preemptive dispatching
 - · Determines processor and network utilization
- · Determines potential of meeting timing constraints

