

Welcome to Lecture 13

Byzantine failures Layered fault tolerance







- A value failure is the same as a content failure
- Both terms are used in the literature
- The term value failure is more commonly used
- The term *content failure* is used by Avizienis et al. in "Basic Concepts and Taxonomy of Dependable and Secure Computing"
- We use both terms in the course

Two types of value failures (from Lecture 2)

- Detectable value failures: the service user(s) can detect the failure
- Non-detectable value failure: the service user(s) cannot detect the failure

Example: Consider a service provider whose outputs are protected by a checksum

Detectable value failure: the output from the service provider has an invalid checksum \Rightarrow a service user can detect the failure by inspecting the checksum Non-detectable value failure: the value of the output is wrong but the output has a valid checksum \Rightarrow a service user cannot detect the failure by inspecting the checksum



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Characterization of failure modes

- Failure domain
 - Content (Value)
- Timing
- Detectability of failures
- Consistency of failures
- Consequences of failures on the environment

See "Basic Concepts and Taxonomy of Dependable and Secure Computing", pp. 18-19.



Agreement in presence of Byzantine failures

- A Byzantine failure is an inconsistent, non-detectable, content/value failure.
- The Byzantine generals problem:
 How do replicated units reach agreement on a non-replicated value in the presence of inconsistent content/value failures?
- This problem is also known as the Interactive consistency problem



Byzantine Generals Problem Scenario

Several armies are camped outside a city which they plan to attack.

Each army is led by a commander. One of the commanders is a general that will issue the order *attack or retreat*.

One or several commanders, including the general, may be traitors.

To win the battle all loyal commanders must attack at the same time.

The traitors will attempt to fool the loyal commanders so that not all of them attack at the same time.

To stop the traitors' malicious plan, all commanders exchange messages directly with each other.



Algorithm ICA(m)

Requirements

- R1: At least 3m+1 units must participate
- R2: At least *m*+1 rounds of communication must take place
- R3: All units must be synchronized within a known skew of each other
- Assumptions about the message passing system
 A1: Every message that is sent by a node is delivered correctly by the message passing system to the receiver.
 - A2: The receiver of a message knows which node has sent the message
 - A3: The absence of a message can be detected.











EDA122/DIT061 Fault-Tolerant Computer Systems

Main features of the Space Shuttle Computer

- Consist of five "off-the-shelf" computers with identical hardware
- Four computer executes critical functions in 4MR*
- The computers exchange results via special inter-processor buses and vote on the results in software
 One computer is a backup equipped with an independently developed**
- One computer is a backup equipped with an independently developed**, stripped-down version of the critical flight control software
- The fifth computer performs non-critical functions under fault-free circumstances
 The computers send independent commands to actuators, which is a send independent commands to actuators.
- The computers send independent commands to actuators, which use hardware voting the mask errors in the commands

* 4MR = NMR, with N=4. ** Design diversity <section-header><section-header><image><image><image>









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 the extent of the corruption of data and the ability to recover from these corruptions (for integrity)





Outline

- Intro to characterization of failure modes
- Byzantine failures
- More on characterization of failure modes
- · Layered fault tolerance

Layered fault tolerance Fault-tolerance is typical achieved by combining several mechanisms or error detection, error masking and system recovery located at *different abstraction layers* in a computer system The division of a system into layers can be done in different ways. We will ok at a simple model with three layers: Hardware layer – mechanisms implemented in hardware either within one integrated circuit, or by replication of integrated circuits within a node. Software layer – mechanisms implemented in software dealing with errors occurring within a node. System layer – mechanisms implemented in software dealing with errors occurring in other nodes in the system, or in the system's communication network.







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