Getting started with VHDL in TDA956

Aimed mostly at those who have not seen VHDL, but worth glancing at for the others

In Week 1

Pair up for the labs with someone who knows VHDL. You can be an attractive partner if you know functional programming ☺

Buy or borrow an Ashenden Designer's Guide to VHDL (or the Swedish alternative, see Literature page). Note the QuickStart slides associated with the book (Intro. lecture material on web page)

There are also numberous VHDL tutorials on the web.

Read the lectures about VHDL by Magnus Björk on the links page. (Comes as 3 pdf files because the .ps presented difficulties)

*Tho*se lectures also give advice about how to tackle the first lab.

Still in Week 1

Read Gaisler's notes about his "two process" approach on the Literature page. Follow this approach.

Go to the supervised Lab Session on Friday morning and PLAY with the VHDL simulator and get help from Emil (TA). Get used to looking at waveforms etc. Concentrate on VHDL this week.

Once you have done this, go back to Björk's VHDL slides and read them again as they will be helpful when starting on the lab.

The tutorial documents available with Modelsim are rather good at describing the "design flow".