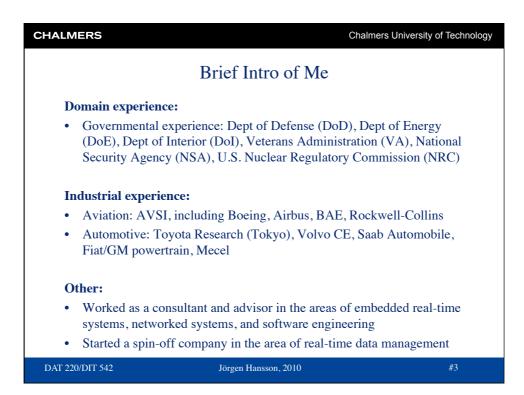
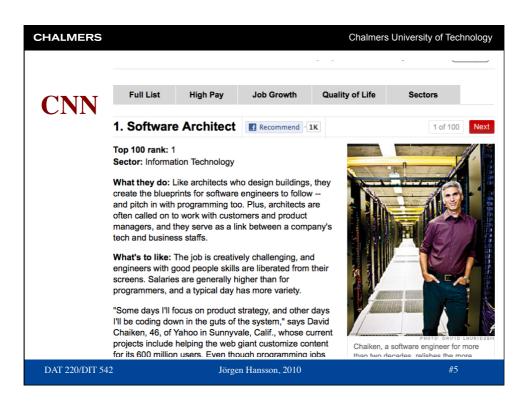
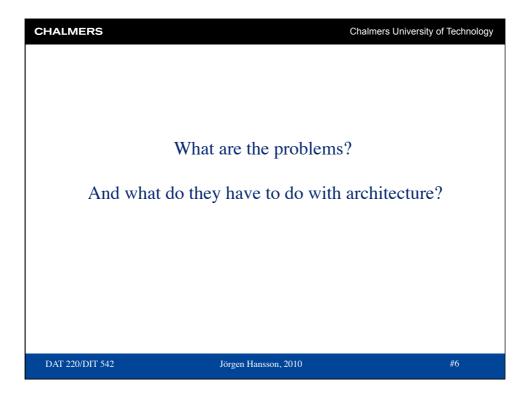


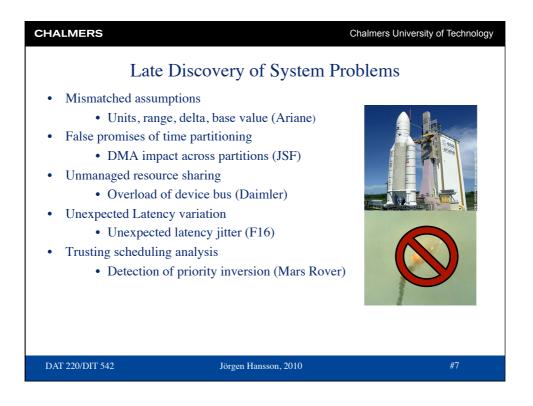
CHALMERS	Chalmers Universi	ty of Technology
	Brief Intro of Me	
Work Expe	rience	
2010 -	Professor at Chalmers in the area of software engine	ering
2005-2010	Senior Member of Technical Staff	
USA	Software Engineering Institute, Carnegie Mellon Un	iv.,
	<ul> <li>Focus on architectural descriptions and validation using the industandard AADL, which was created by the SEI.</li> </ul>	stry
2000-2007	Professor at Linköping University in the area of real- systems	time
	<ul> <li>Focus on management of real-time data in embedded real-time synchronized including QoS/QoD, real-time component models, database system</li> </ul>	
DAT 220/DIT 542	Jörgen Hansson, 2010	#2



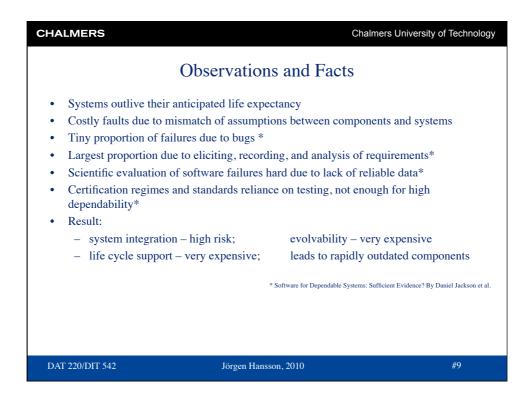
CHALMERS	Cha	almers University of Technology
	Outline	
<ul><li>I.e., what are</li><li>Designing an</li><li>Architectural</li></ul>	ationale and purpose of architecting the problems architecting aims to a d Architecting next generation aircr	address raft
DAT 220/DIT 542	Jörgen Hansson, 2010	#4

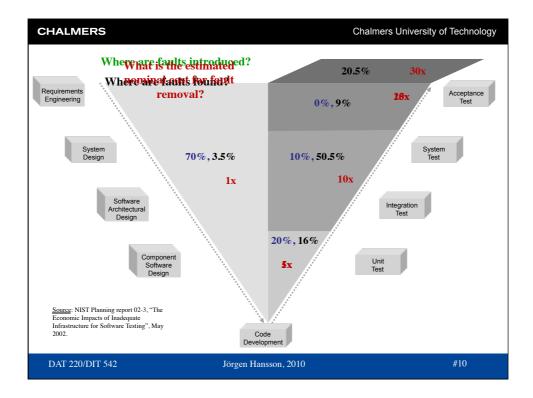




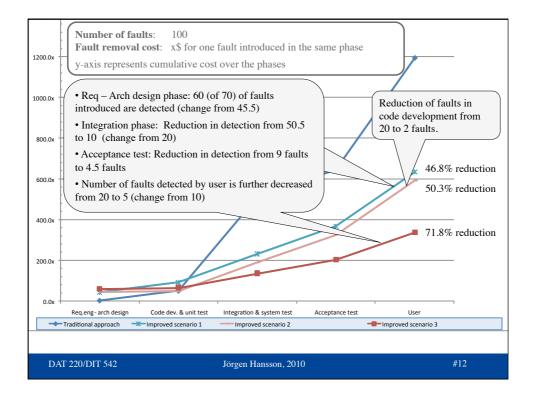


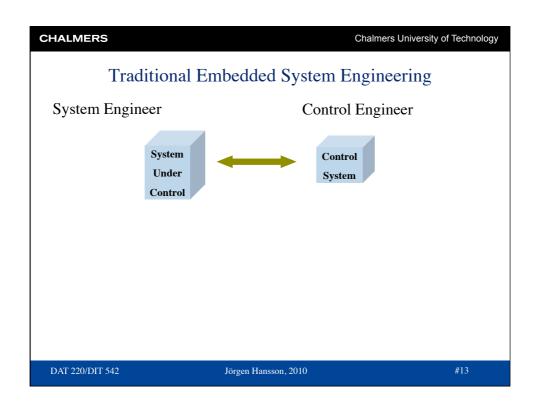
CHALMERS	Chalmers University of Technology
System Level Fault Root Ca         Violation of data stream assumptions         - Stream miss rates, mismatched data representation, late         Partitions as Isolation Regions         - Space, time, and bandwidth partitioning         - Isolation not guaranteed due to undocumented resource         - Fault containment, security levels, safety levels, distrib         Virtualization of time & resources         - Logical vs. physical redundancy         - Time stamping of data & asynchronous systems         •Inconsistent System States & Interactions         - Modal systems with modal components         - Concurrency & redundancy management         - Application level interaction protocols	ency jitter & age e sharing
DAT 220/DIT 542 Jörgen Hansson, 2010	#8

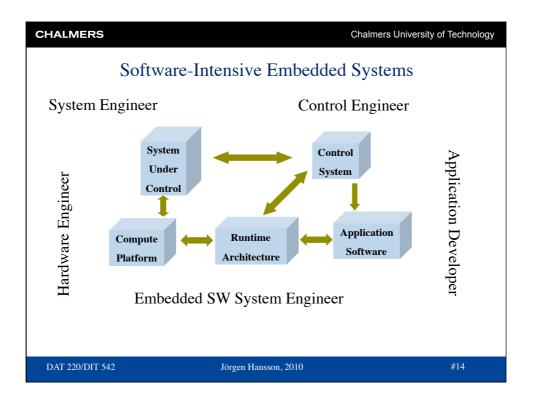


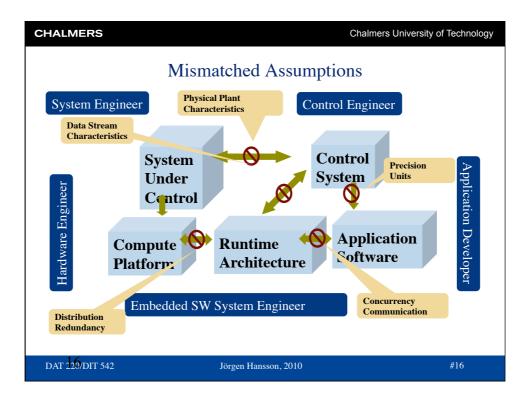


HALMERS				(	Chalmers Uni	iversity of Technol
Defect Economics						
Phase	Defects Relative defect removal cost originating in of each phase of origin					
Thuse	phase (%)	Req's	Design	Unit test	Integration	Documentation
Requirements	15%	1				
Design	35%	2.5	1			
Unit coding	30%	6.5	2.5	1		
Integration	10%	16	6.4	2.5	1	
Documentation	10%					1
System/Accep- tance test	-	40	16	6.2	2.5	2.5
Operation	N/A	110	44	17	6.9	6.8
Source: D. Galin, "Software Quality Assurance: From Theory to Implementation", Pearson/Addison-Wesley (2004) & B.W. Boehm, "Software Engineering Economics", Prentice Hall (1981)						
DAT 220/DIT 542		Jörgen Hanss	son, 2010			#11

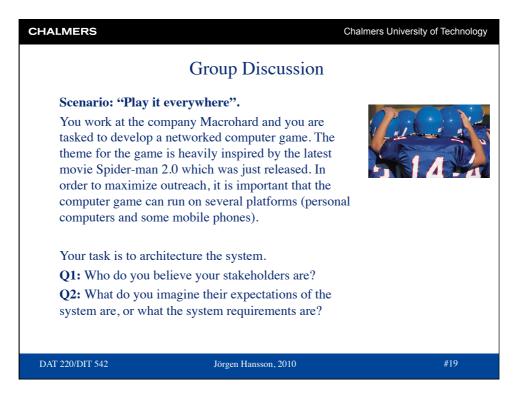




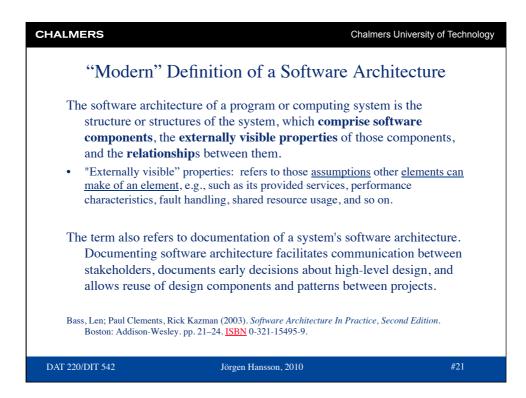






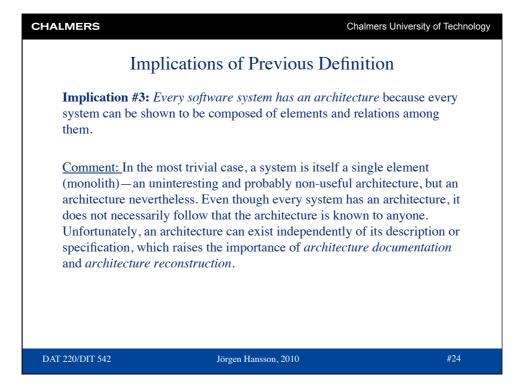


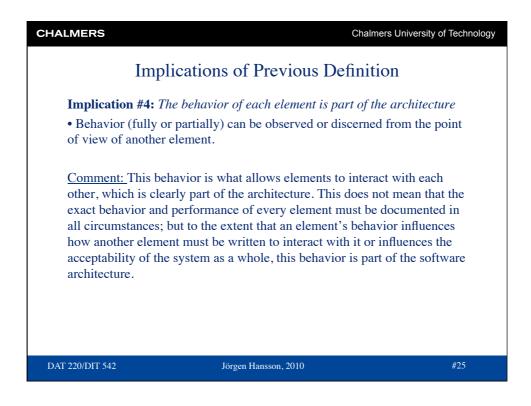
CHALMERS	Chalme	ers University of Technology		
	"Modern" Definitions			
<ul> <li>ANSI/IEEE Std 1471-2000, Recommended Practice for Architectural Description of Software-Intensive Systems</li> <li>Architecture is defined by the recommended practice as <u>the fundamental</u> organization of a system, embodied in its components, their relationships to</li> </ul>				
each other and the evolution.	e environment, and the principles governing	its design and		
architecture by re among these is th	intended to encompass a variety of uses of the cognizing their underlying common element the need to understand and control those element encode to system's utility, cost, and risk. In some	s. Principal ents of system		
elements are the j other cases, these In still other cases	<u>physical components</u> of the system and <u>their</u> elements are not physical, but instead, <u>logic</u> s, these elements are enduring principles or p <u>rganizational structures</u> .	relationships. In al components.		
DAT 220/DIT 542	Jörgen Hansson, 2010	#20		



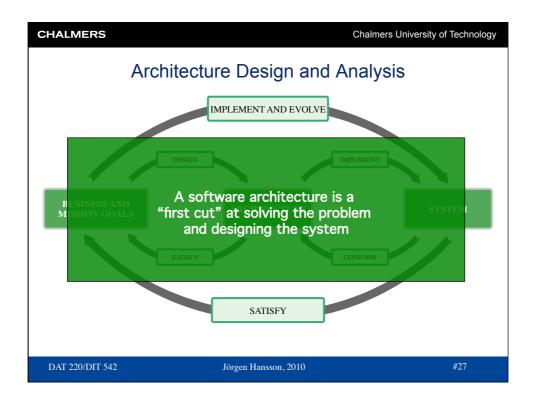
CHALMERS	Chalmers	s University of Technology
Impli	cations of Previous Definitio	n
<ul> <li>The architecture to each other. Th <u>information</u> about</li> <li>Thus, an architect</li> </ul>	chitecture defines elements. embodies information about how the ele is means that <u>architecture specifically on</u> it elements that does not pertain to their ture is foremost an <i>abstraction</i> of a syste s of elements that do not affect how they	<i>nits</i> certain interaction. em that
<ul> <li>by, relate to, or interact with other elements.</li> <li>In nearly all modern systems, elements interact with each other by means of interfaces that partition details about an element into public and private parts. Architecture is concerned with the public side of this division; private details of elements—details having to do solely with internal implementation—are not architectural</li> </ul>		
DAT 220/DIT 542	Jörgen Hansson, 2010	#22

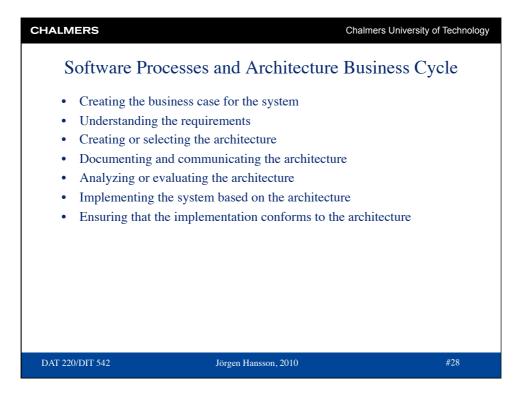
CHALMERS		Chalmers University of Technology
Implication	ns of Previous De	finition
Implication #2: Systems co	an and do comprise mor	e than one structure .
• No single structure holds t	he irrefutable claim to b	being the architecture.
<u>Comment:</u> For example, all implementation units; these are the basis of work assign element will comprise progr implementation units can ca private. In large projects, th for assignment to sub-teams describe a system. It is a ver the system's functionality is teams.	units are given specific ments for programming rams and data that softwall or access, and program e elements will almost c s. This is one kind of stru- ry static structure, in that	responsibilities, and teams. This kind of vare in other ms and data that are vertainly be subdivided ucture often used to t it focuses on the way
DAT 220/DIT 542	Jörgen Hansson, 2010	#23

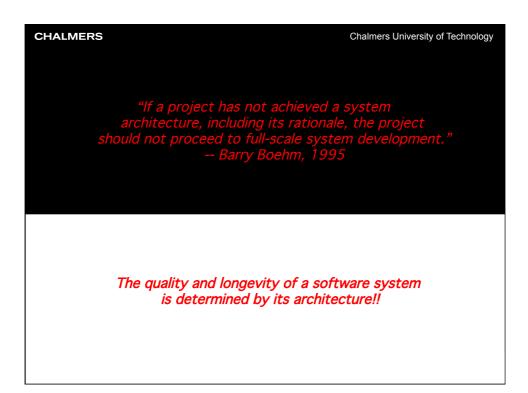




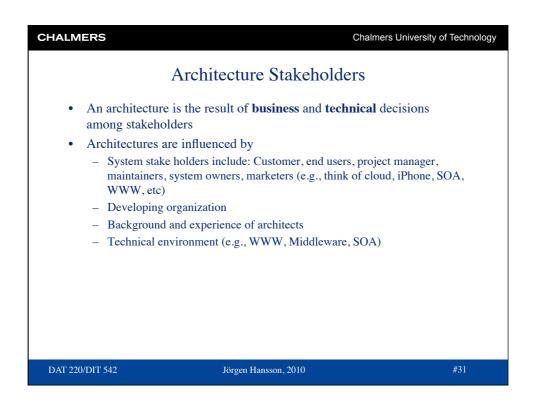
<ul> <li>Implications of Previous Definition</li> <li>Implication #5: Definition is indifferent as to whether a system is a good one or a bad one,</li> <li>Architecture will allow or prevent the system from a behavioral, performance, and life-cycle requirement</li> <li>Architecture Evaluation is important <ul> <li>Assuming that we do not accept trial and error a choose an architecture for a system—that is, pic architecture at random, building the system from the best—this raises the importance of architecture</li> </ul> </li> </ul>	the architecture for neeting its
<ul> <li>a system is a good one or a bad one,</li> <li>Architecture will allow or prevent the system from a behavioral, performance, and life-cycle requirement</li> <li>Architecture Evaluation is important <ul> <li>Assuming that we do not accept trial and error a choose an architecture for a system—that is, pic architecture at random, building the system from</li> </ul> </li> </ul>	neeting its
<ul> <li>behavioral, performance, and life-cycle requirement</li> <li>Architecture Evaluation is important <ul> <li>Assuming that we do not accept trial and error a choose an architecture for a system—that is, pic architecture at random, building the system from</li> </ul> </li> </ul>	6
<ul> <li>Assuming that we do not accept trial and error a choose an architecture for a system—that is, pic architecture at random, building the system from</li> </ul>	
choose an architecture for a system—that is, pic architecture at random, building the system from	
	king an n it, and hoping for
DAT 220/DIT 5/2	
DAT 220/DIT 542 Jörgen Hansson, 2010	#26



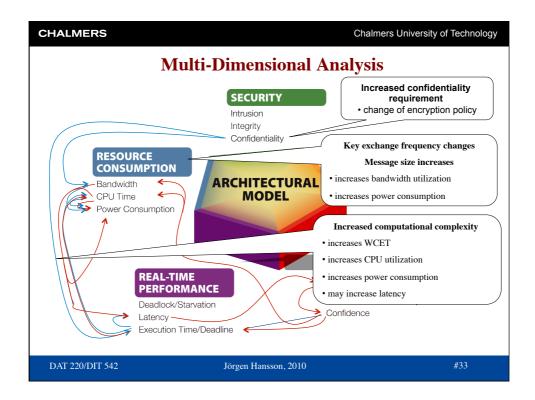


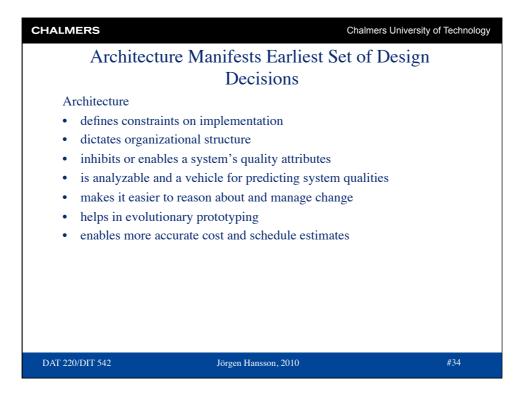


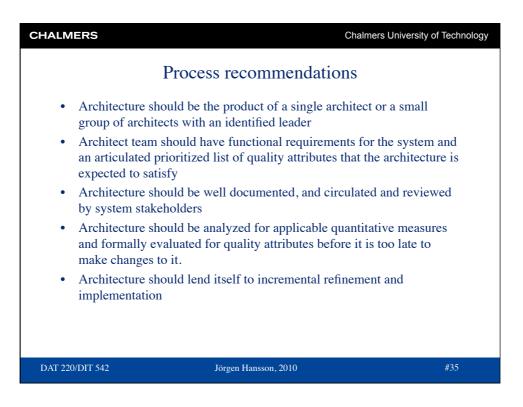
CHALMERS	Chalmers University of Technology
Why is an Architect	÷
The Technical	Perspective
Communication among stakeholders	
<ul> <li>Software architecture represents a con most if no all of the system's stakehol- understanding, negotiation, consensus</li> </ul>	ders can use as a basis for mutual
• Early design decisions	
<ul> <li>Software architecture manifests the ea system, and these early bindings carry individual gravity with respect to the s deployment, and its maintenance life.</li> </ul>	weigh far out of proportion to their
• Transferable abstraction of a system	
<ul> <li>Software architecture constitutes a relagraspable model for how a system is s together.</li> </ul>	
<ul> <li>The model is transferable across syste systems exhibiting similar quality attri thus promoting large-scale reuse.</li> </ul>	
DAT 220/DIT 542 Jörgen Hansson, 2	010 #30



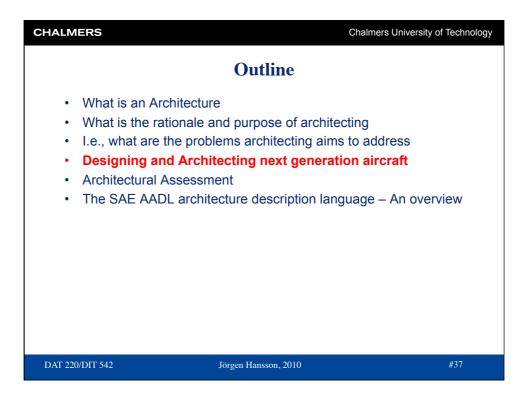
CHALMER	as	Chalmers University of Technology
	Functional vs. Non-Functiona	l behavior
(i)	Functional behavior	
( <b>ii</b> )	Non-functional behavior (aka quality attributes, ex	tra-functional behavior)
-	- Performance: real-time	
	- Security	
	- Reliability	
	- Availability	
-	- Maintainability	
	– Evolvability	
-	- X-ility	
	ervation #1: Functional behavior gives the "uniquer m Non-functional behavior drives the perceived m	
	rvation #2: Many quality attributes are system attri are and hardware.	butes, i.e., it involves
Obse	rvation #3: Quality attributes are not independent	
DAT 220/DI	T 542 Jörgen Hansson, 2010	#32



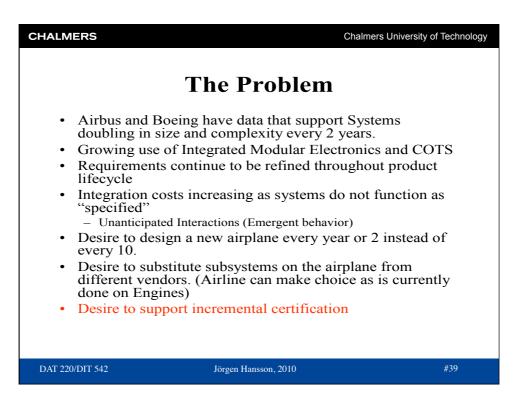




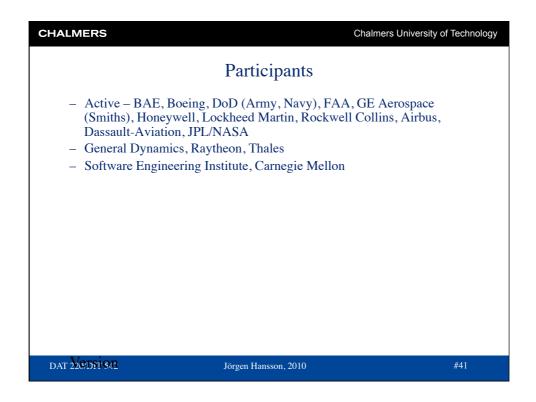
CHALMERS	Chalmers University of Technology
<ul> <li>Blackboard</li> <li>Client-server (2-tier, n-tier, peer-topeer, Cloud Computing all use this model)</li> <li>Database-centric architecture (broad division can be made for programs which have database at its center and applications which don't have to rely on databases, E.g. desktop application programs, utility programs etc.)</li> <li>Distributed computing</li> </ul>	<ul> <li>Chalmers University of Technology</li> <li>ral Patterns</li> <li>Peer-to-peer</li> <li>Pipes and filters</li> <li>Plugin</li> <li>Representational State Transfer</li> <li>Rule evaluation</li> <li>Search-oriented architecture (A pure SOA implements a service for every data access point)</li> <li>Service-oriented architecture</li> <li>Shared nothing architecture</li> <li>Software componentry (strictly module-based, usually object-</li> </ul>
<ul><li>Event Driven Architecture</li><li>Front-end and back-end</li><li>Implicit invocation</li><li>Monolithic application</li></ul>	<ul> <li>oriented programming within modules, slightly less monolithic)</li> <li>Space based architecture</li> <li>Structured (module-based but</li> </ul>
DAT 220/DIT 542 Jörgen Hans	sson, 2016 ually monolithic within modul#36



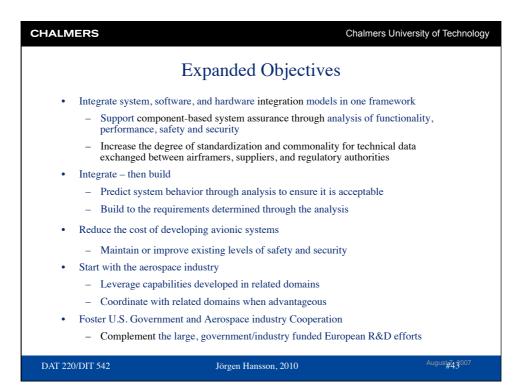
CHALMERS		Chalmers University of Technology	
Does Model-Based Development Scale?			
Airbus A	380	<ul> <li>Systems Developed Using MBD</li> <li>Flight Control</li> <li>Auto Pilot</li> <li>Flight Warning</li> <li>Cockpit Display</li> <li>Fuel Management</li> </ul>	
Length Wingspan Maximum Takeoff Weight Passengers Range	239 ft 6 in 261 ft 10 in 1,235,000 lbs Up to 840 9,383 miles	<ul> <li>Landing Gear</li> <li>Braking</li> <li>Steering</li> <li>Anti-Icing</li> <li>Electrical Load Management</li> </ul>	
DAT 220/DIT 542	Jörgen Han	sson, 2010 #38 38	

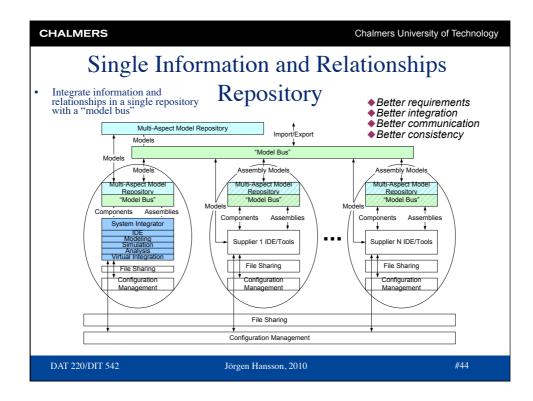


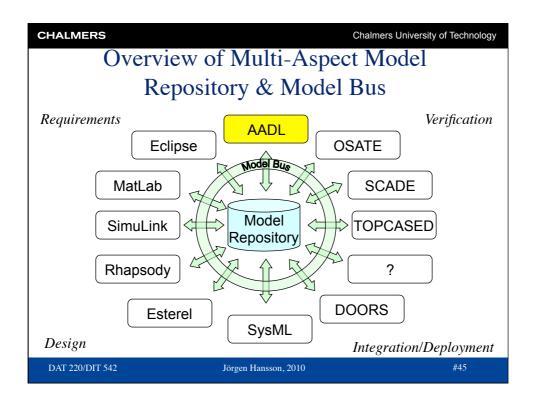


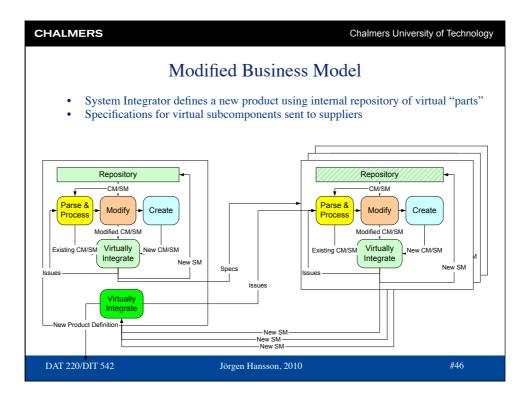


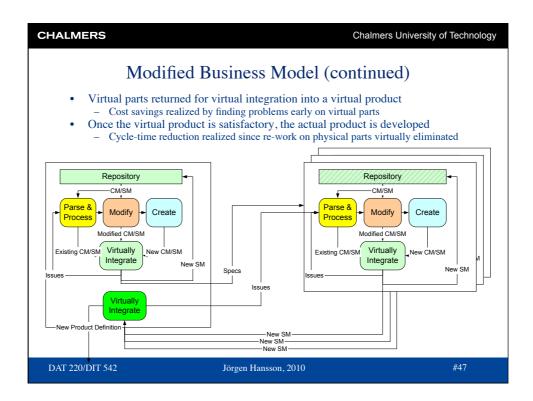
CHALMERS	Chalmer	rs University of Technology
	Project Overview	
	pt of Operations d production based on early and continuou physical)	is integration
<ul><li>Integrate,</li><li>Objective</li></ul>	then build	
– Shift archi	tecting, design, and production activities t tegration issues early, reducing program es and cost	
Approach		
developme	elop "integration-based" software and sys ent processes with emphasis on integrating del-based and proof-based development	
DAT 220/DIT 542	Jörgen Hansson, 2010	#42

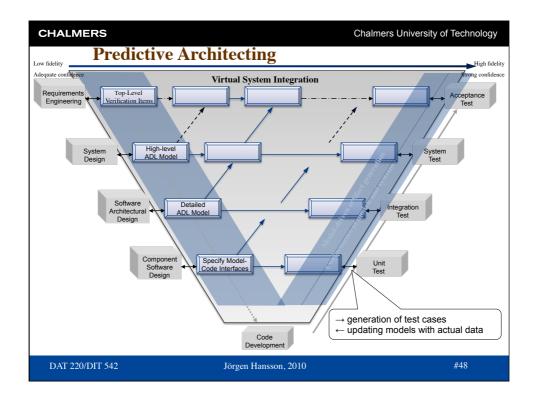


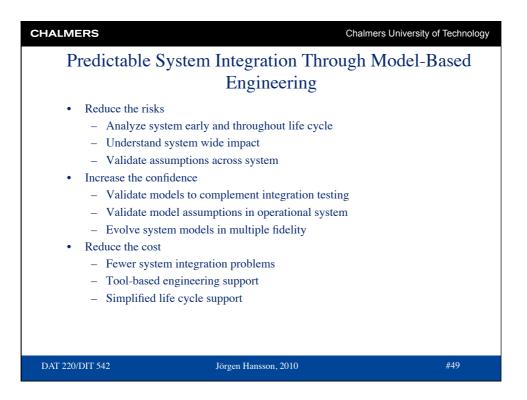


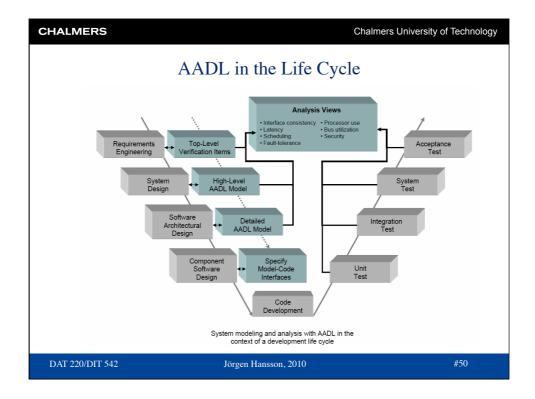


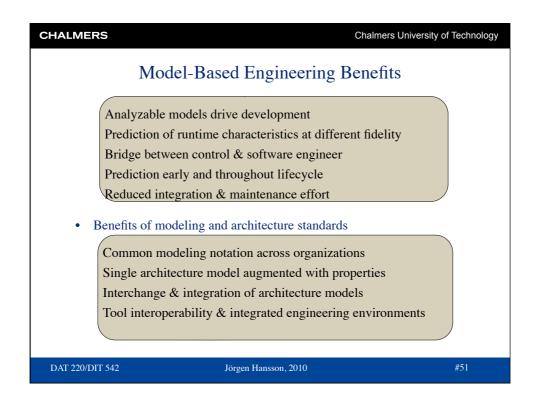


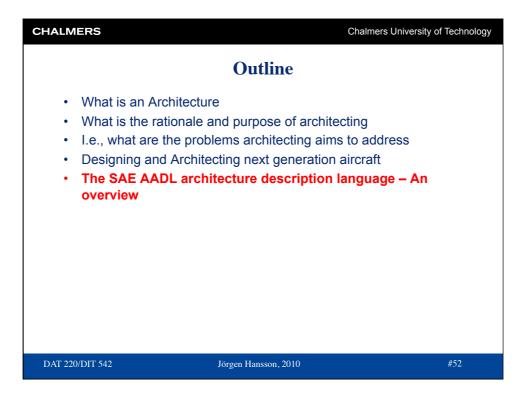


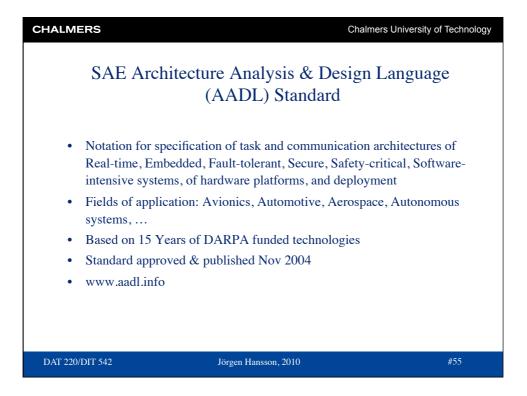




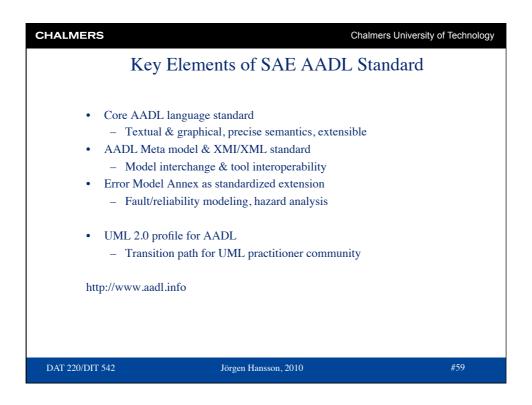




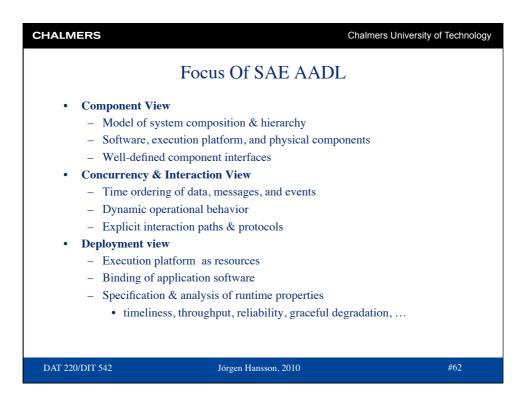




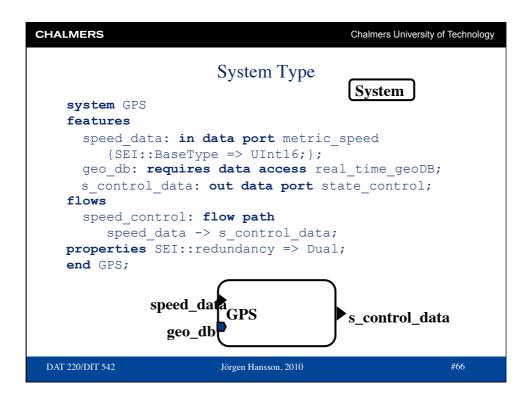
CHALMERS	Chalmers University of Technology
AADI	in Context
<ul> <li>Behavioral validation</li> <li>ADL Interchange</li> <li>ACME</li> <li>UML Profile</li> </ul>	DARPA Funded Research since 1990 Amilia Bassis Extensible Real-time Dependable Company Mancement Mancement
DAT 220/DIT 542 Jörgen H	ansson, 2010 #57



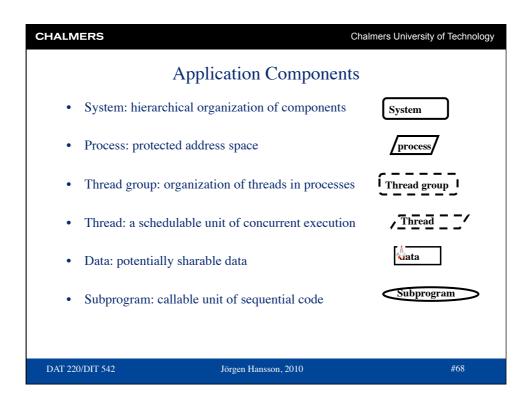
CHALMERS	Chalmers University of Technology	
AADL: 7	The Language	
<ul> <li>Precise execution semantics for <ul> <li>Thread, process, data, subprog</li> </ul> </li> <li>Continuous control &amp; event resp <ul> <li>Data and event flow, synchrone</li> <li>End-to-End flow specifications</li> </ul> </li> </ul>	ram, system, processor, memory, bus, device ponse processing pus call/return, shared access	
<ul> <li>Operational modes &amp; fault toler</li> <li>Modes &amp; mode transition</li> </ul>	ant configurations	
<ul> <li>Modeling of large-scale system:</li> <li>Component variants, packaging</li> </ul>		
<ul> <li>Accommodation of diverse analysis needs         <ul> <li>Extension mechanism, standardized extensions</li> </ul> </li> </ul>		
DAT 220/DIT 542 Jörgen Ha	nsson, 2010 #60	

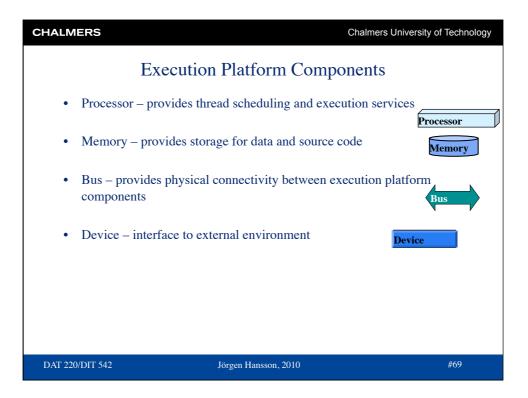


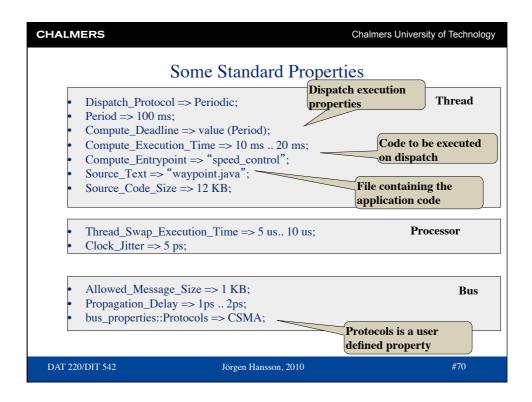
CHALMERS Chaim	ners University of Technology	
Predictable System Integration Through Model-Based		
Engineering		
• Reduce the risks		
<ul> <li>Analyze system early and throughout life cycle</li> </ul>		
<ul> <li>Understand system wide impact</li> </ul>		
<ul> <li>Validate assumptions across system</li> </ul>		
• Increase the confidence		
<ul> <li>Validate models to complement integration testing</li> </ul>		
<ul> <li>Validate model assumptions in operational system</li> </ul>		
- Evolve system models in multiple fidelity		
• Reduce the cost		
<ul> <li>Fewer system integration problems</li> </ul>		
<ul> <li>Tool-based engineering support</li> </ul>		
<ul> <li>Simplified life cycle support</li> </ul>		
DAT 220/DIT 542 Jörgen Hansson, 2010	#63	



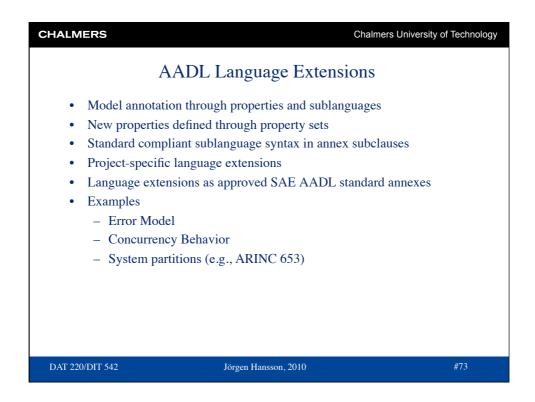
CHALMERS	Chalmers U	niversity of Technology	
System Implementation			
encoder: system P receiver: system connections cl: data port spe	<pre>On GPS.secure PGP_decoder.basic; PGP_encoder.basic; GPS_receiver.basic; eed_data -&gt; decoder.in; coder.out -&gt; receiver.in;</pre>		
<pre>c3: data port red c4: data port end flows</pre>	<pre>seiver.out -&gt; encoder.in; soder.out -&gt; s_control_data;</pre>		
-> c2 -> c4	<pre>w path speed_data -&gt; cl -&gt; c -&gt; receiver.fs1 -&gt; c3 -&gt; end -&gt; s_control_data;</pre>		
<pre>modes none; properties SEI::red end GPS.secure;</pre>	lundancy_scheme => Primary_Ba	ackup;	
DAT 220/DIT 542	Jörgen Hansson, 2010	#67	







CHALMERS		Chalmers University of Technology
Component Interactions & Modes		
<ul> <li>Port-base</li> <li>State</li> <li>Flow</li> <li>End-t</li> </ul>	data, events, messages specifications & connections o-end flows ous call/return	
<ul> <li>Modal &amp; dynamically configurable systems</li> <li>Modeling of operational modes</li> <li>Modeling of fault tolerant configurations</li> <li>Modeling of different levels of service</li> </ul>		
DAT 220/DIT 542	Jörgen Hansson, 2010	#71



CHALMERS	Chalmers University of Technology		
Airbus Annex Extension			
THREAD t FEATURES sem1 : DATA ACCESS semaphore; sem2 : DATA ACCESS semaphore; END t; THREAD IMPLEMENTATION t.t1 PROPERTIES Period => 13.96ms; cotre::Priority => 1; cotre::Phase => 0.0ms; Dispatch_Protocol => Periodic; COTRE thread properties	ANNEX cotre.behavior {** STATES s0, s1, s2, s3, s4, s5, s6, s7, s8 : STATE; s0 : INITIAL STATE; TRANSITIONS s0 -[]> s1 { PERIODIC_WAIT }; s1 -[]> s2 { COMPUTATION(1.9ms, 1.9ms) }; s2 -[ sem1.wait !(-1.0ms) ]> s3; s3 -[]> s4 { COMPUTATION(0.1ms, 0.1ms) }; s4 -[ sem2.wait !(-1.0ms) ]> s5; s5 -[]> s6 { COMPUTATION(0.1ms, 0.1ms) }; s6 -[ sem2.release !]> s7; s7 -[]> s8 { COMPUTATION(1.5ms, 1.5ms) }; s8 -[ sem1.release !]> s0; **}; END t.t1; COTRE behavioral annex Courtesy of		
DAT 220/DIT 542 Jörgen H	Hansson, 2010 #74		

