





# **Real-Time Systems**

Lecture #5

#### **Professor Jan Jonsson**

Department of Computer Science and Engineering Chalmers University of Technology





# **Real-Time Systems**







### **Mutual exclusion**

Systems with cooperating concurrent tasks often work with <u>shared data structures</u>.

- A problem that has to be solved is then how to guarantee that the data structure is always kept in a <u>consistent state</u>.
   Data structures such as queues, lists and data bases will not work as intended if their state becomes inconsistent.
- A working solution is achieved if one makes sure that only one task at a time receive access to the data structure.
- Exclusive access to a data structure can be achieved by making sure that the program code (i.e., the critical region) that manipulates the data structure can execute without being preempted in the most critical moment.



#### UI 🌔

## Example: circular buffer in TinyTimber

```
// Define a class Circular Buffer with space for 8 natural numbers (\geq 0)
#define BSize 8
                                                               Unused slots
typedef struct {
                                                            🖉 Stored data
   Object
            super;
    int
         count;
        I;
    int
    int
             J;
        A[BSize];
    int
} Circular Buffer;
                                                    Т
                                                             J
// If the buffer is full, Put should return the value -1.
// If the buffer is empty, Get should return the value -1.
int Put(Circular Buffer*, int); // Insert new element
int Get(Circular Buffer*, int); // Remove old element
// Define an instance of the buffer
Circular Buffer Buf = { initObject(), 0, 0, 0 }; // empty buffer
```





### Example: circular buffer in TinyTimber

```
int Put(Circular Buffer *self, int data) {
    if (self->count < BSize) {</pre>
        self->A[self->I] = data;
        self->I = (self->I + 1) % BSize;
        self->count = self->count + 1;
        return 0;
    }
    else
        return -1;
}
int Get(Circular Buffer *self, int unused) {
    if (self->count > 0) {
        int data = self->A[self->J];
        self->J = (self->J + 1) % BSize;
        self->count = self->count - 1;
        return data;
    }
    else
        return -1;
```









In TinyTimber the methods Put or Get must be called using SYNC() in order to guarantee mutual exclusion.

- If Put or Get would be called as regular functions in C, mutual exclusion can not be guaranteed.
- In the latter case, the buffer data structure could very easily become corrupt and give rise to data inconsistencies.

The following example demonstrates one such case ...



Assume that the buffer has the following state:



Now, investigate what happens if Put is called as a regular C function by two concurrent tasks:

```
void T1(App *self, int c) {
    ...
    Put(&Buf,X);
    ...
    ASYNC(self,T1,c);
}
void T2(App *self, int c) {
    ...
    Put(&Buf,Y);
    ...
    ASYNC(self,T1,c);
}
```





#### The following execution order causes data inconsistency:

Put(&Buf,Y):	Comment:
A(I) = Y;	// X is overwritten
I = (I + 1) % BSize;	
count = count + 1;	
	<pre>// old value remains</pre>
	// in last data slot
	<pre>Put(&amp;Buf,Y): A(I) = Y; I = (I + 1) % BSize; count = count + 1;</pre>

### What we want is consistent data:



### What we get is inconsistent data:





Again, assume that the buffer has the following state:



This time observe the result when Put is called using SYNC() (synchronous method call) by the two concurrent tasks:

```
void T1(App *self, int c) {
    ...
    SYNC(&Buf,Put,X);
    ...
    ASYNC(self,T1,c);
    }
    void T2(App *self, int c) {
    ...
    SYNC(&Buf,Put,X);
    ...
    ASYNC(self,T1,c);
    }
}
```





# **Mutual exclusion**

#### With SYNC() we get data consistency:



What we want is consistent data:



#### What we get is consistent data:





## **Machine-level mutual exclusion**

To guarantee mutual exclusion in the critical regions of e.g. semaphore operations or mutex methods some even more fundamental support is needed.

- For this purpose there are two mechanisms offered at the lowest (machine-code) level:
- Disabling the processor's interrupt service mechanism
  - Should involve any interrupt that may lead to a task switch
  - Only suitable for single-processor systems
- Atomic processor instructions
  - For example: the test-and-set instruction
  - Variables can be tested and updated in one operation
  - Necessary for systems with two or more processors



# **Disabling processor interrupts**

- In single-processor systems, the mutual exclusion is guaranteed by disabling the processor's interrupt service mechanism ("interrupt masking") while the critical region is executed.
- This way, unwanted task switches in the critical region (caused by e.g. timer interrupts) are avoided. However, <u>all other</u> tasks are unable to execute during this time.
- Therefore, critical regions should only contain such instructions that really require mutual exclusion (e.g., code that handles the operations wait and signal for semaphores).
  - Note: this method is not used in multi-processor systems since interrupt management is typically not synchronized between the processors.



## **Disabling processor interrupts**

```
task A;
task B;
task body A is
begin
 Disable_Interrupts; -- turn off interrupt handling
                          -- critical region
  . . .
 Enable_Interrupts; -- A leaves critical region
                           -- remaining program code
  . . .
end A;
task body B is
begin
 Disable_Interrupts; -- turn off interrupt handling
                          -- critical region
 Enable Interrupts;
                           -- B leaves critical region
                           -- remaining program code
  . . .
end B;
```



# **Atomic processor instruction**

- In multi-processor systems with shared memory, a test-and-set instruction is used for handling critical regions.
- A test-and-set instruction is a processor instruction that reads from and writes to a variable in one atomic operation.
- The functionality of the test-and-set instruction can be illustrated by the following Ada procedure:

The combined read and write of lock must be atomic. In a multiprocessor system, this is guaranteed by locking (disabling access to) the memory bus during the entire operation.



### **Atomic processor instruction**

```
lock : Boolean := false; -- shared flag
task A, B;
task body A is
 previous : Boolean;
begin
  loop
   testandset(lock, previous); -- A waits if critical region is busy
  exit when not previous;
  end loop;
                                    -- critical region
  . . .
 lock := false;
                                    -- A leaves critical region
                                    -- remaining program code
  . . .
end A;
task body B is
 previous : Boolean;
begin
  loop
   testandset(lock, previous); -- B waits if critical region is busy
  exit when not previous;
 end loop;
                                    -- critical region
  . . .
 lock := false;
                                    -- B leaves critical region
                                    -- remaining program code
  . . .
end B;
```



### **Atomic processor instruction**

```
lock : Boolean := false;
                              -- shared flag
task A, B;
task body A is
  previous : Boolean;
begin
  loop
    testandset(lock, previous);
                                    -- A waits if critical region is busy
  exit when not previous;
  end loop;
                                    -- critical region
  . . .
 lock := false;
                                    -- A leaves critical region
                                    -- remaining program code
  . . .
end A;
task body B is
  previous : Boolean;
begin
  loop
    testandset(lock, previous);
                                    -- B waits if critical region is busy
  exit when not previous;
  end loop;
                                    -- critical region
  lock := false;
                                    -- B leaves critical region
                                    -- remaining program code
end B;
```





# **Call-back functionality**

Operations for resource management:

- acquire: to request access to a resource
- release: to release a previously acquired resource

The acquire operation can be either blocking or non-blocking:

- <u>Blocking</u>: the task that calls acquire is blocked if the resource is not available. Blocked tasks are stored in a queue, in FIFO or priority order. When the requested resource becomes available one of the blocked tasks is unblocked and is activated via a *callback functionality*.
- <u>Non-blocking</u>: acquire returns a status code to the calling task indicating whether access to the resource was granted or not.

To support the reactive programming paradigm (that is, no "busy waiting" code) we should use the blocking approach.





# **Call-back functionality**

#### Protected objects:

```
protected type Exclusive_Resource is
    entry Acquire;
    procedure Release;
private
    Busy : Boolean := false;
end Exclusive Resource;
```

```
protected body Exclusive_Resource is
  entry Acquire when not Busy is
  begin
   Busy := true;
  end Acquire;
```

```
procedure Release is
begin
Busy := false;
end Release;
end Exclusive Resource;
```

If task blocks here, <u>call-back</u> <u>information</u> must be saved in order to wake up the task later.





# **Call-back functionality**

#### Monitors:

```
public void Acquire() {
    lock.lock();
    try {
      while (busy)
        notBusy.await();
                             // block the task if resource busy
      Busy = true;
    } finally {
      lock.unlock();
                                         If task blocks here, call-back
                                         information must be saved in
                                         order to wake up the task later.
 public void Release() {
    lock.lock();
    Busy = false;
                             // manipulate internal state of monitor
    notBusy.signal();
                             // then wake up a blocked task (if one exists)
    lock.unlock();
  l
} // class Exclusive Resource
```





# **Call-back functionality**

#### Semaphores:

```
protected type Semaphore (InitialValue : Natural := 0) is
    entry Wait;
    procedure Signal;

private
    Value : Natural := InitialValue;
end Semaphore;

protected body Semaphore is
    entry Wait when Value > 0 is
    begin
    Value := Value - 1;
end Wait;
```

procedure Signal is
begin
Value := Value + 1;
end Signal;
end Semaphore;

If task blocks here, <u>call-back</u> <u>information</u> must be saved in order to wake up the task later.





# **Call-back functionality**

Call-back information:

- As shown in the previous examples, the implementation of resource management mechanisms such as protected objects, monitors and semaphores make use of <u>call-back</u> <u>information</u> to be able to wake up a blocked task when the requested resource becomes available.
- Since multiple tasks may want to request access to a resource that is currently unavailable, call-back information for each of these tasks must be stored in a suitable data structure, e.g., a queue.



# **Call-back functionality**

Call-back functionality in TinyTimber:

- TinyTimber has inherent method call blocking and call-back functionality, via the SYNC() call, in its implementation of an object (with its internal state) as an <u>exclusive</u> resource.
- However, TinyTimber cannot perform blocking or call back based on <u>conditions relating to the contents of an object</u>.

If a generic acquire/release type of mechanism for <u>shared</u> resources, such as semaphores, is to be added to TinyTimber a separate call-back functionality must be implemented for that mechanism (= this week's exercise).

• TinyTimber also has call-back functionality in the *device drivers* for the serial port and CAN interfaces, in support of the reactive programming paradigm.





# **Call-back functionality**

Device driver programming:

- A <u>device driver</u> is a software module that allows the user to interact with peripheral devices, such as serial ports or network interfaces, in a hardware-independent fashion.
- The device driver conceals the details in the cooperation between software and hardware by defining a set of operations on the device, e.g., *initialize*, *read*, and *write*.
- The device driver also contains handler code for any hardware interrupt that may be associated with the peripheral device. If a task may block while waiting for an event to happen on the device, e.g., data becomes available, the interrupt handler will require call-back information from the user of the device.



Guidelines for interrupt handling in TinyTimber:

- Interrupts must be handled using objects.
- An interrupt handler must be written as a <u>method</u> in the object.
- Data being processed by the interrupt handler must be stored in state variables in the object.
- Reading and writing such data from the user's program code must be done via synchronous calls to methods in the object, i.e., SYNC() calls.

We will now study the device driver for the serial port (SCI) in more detail.



Example: implementing an SCI interrupt handler:

- 1. Define class Serial, and add state variables for:
  - the hardware base address of the device
  - call-back information for a method if data received by the handler needs to be taken care of by the user-level code (the call back should be done using an ASYNC () call)
  - necessary local storage (buffers, queues, etc)
- 2. Define a symbol SCI\_PORT0 representing the hardware base address of the device.

#define SCI\_PORT0 device\_hardware\_address

- 3. Create an object sci0 of class Serial, and initialize it with:
  - the hardware base address SCI PORTO
  - any possible call-back information



Example: implementing an SCI interrupt handler (cont'd): In file 'application.c':

```
App app = { initObject(), 0, 'X' };
void reader(App*, int);
Serial sci0 = initSerial(SCI_PORT0, &app, reader);
void reader(App *self, int c) { // call-back function
        SCI_WRITE(&sci0, "Rcv: \'");
        SCI_WRITECHAR(&sci0, c);
        SCI_WRITE(&sci0, "\'\n");
}
```





Example: implementing an SCI interrupt handler (cont'd):

- 4. Write an interrupt handler as a method sci\_interrupt and associate it with the object.
- 5. Declare a symbol SCI\_IRQ0 and assign to it the TinyTimber kernel's logical number of the hardware interrupt: #define SCI IRQ0 interrupt logical number

6. Inform the TinyTimber kernel that the method is a handler for interrupt SCI IRQ0, by making a call to

INSTALL(&sci0, sci\_interrupt, SCI\_IRQ0);

This should be done <u>before</u> the call to **TINYTIMBER()** 





Example: implementing an SCI interrupt handler (cont'd):

- 7. Provide an operation SCI\_INIT() that takes care of performing any remaining initialization of the device.
- 8. Call SCI\_INIT() in the "kick-off" method that was supplied as argument to the TINYTIMBER() call.





Example: implementing an SCI interrupt handler (cont'd): In file 'application.c':

```
void startApp(App *self, int arg) {
    SCI_INIT(&sci0);
    SCI_WRITE(&sci0, "Hello, hello...\n");
    ...
}
int main() {
    INSTALL(&sci0, sci_interrupt, SCI_IRQ0);
    TINYTIMBER(&app, startApp, 0);
}
```