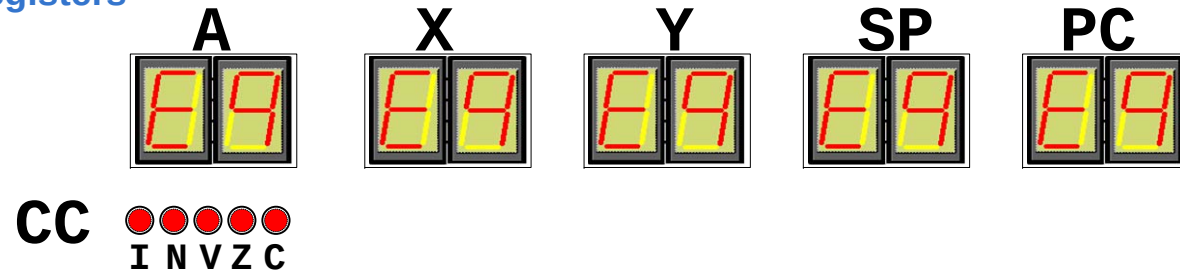


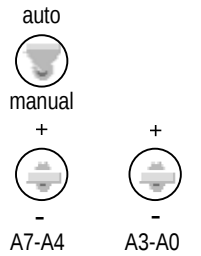
FLIS PROCESSOR

Type 1, Version 2

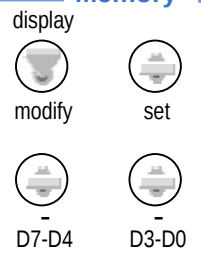
Registers



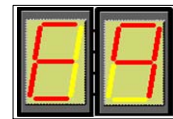
Address



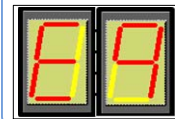
Memory



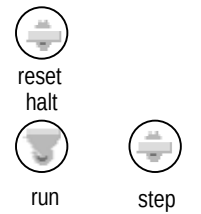
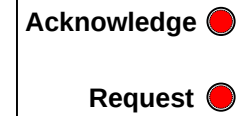
Memory address



Memory data

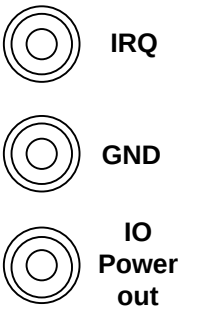
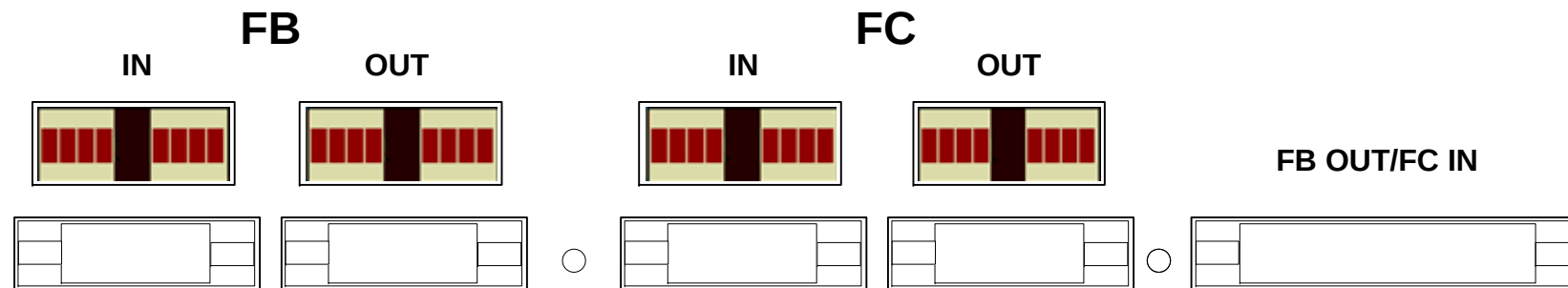


Interrupt



Execute

Input/Output



Connections

LU4-FLISP