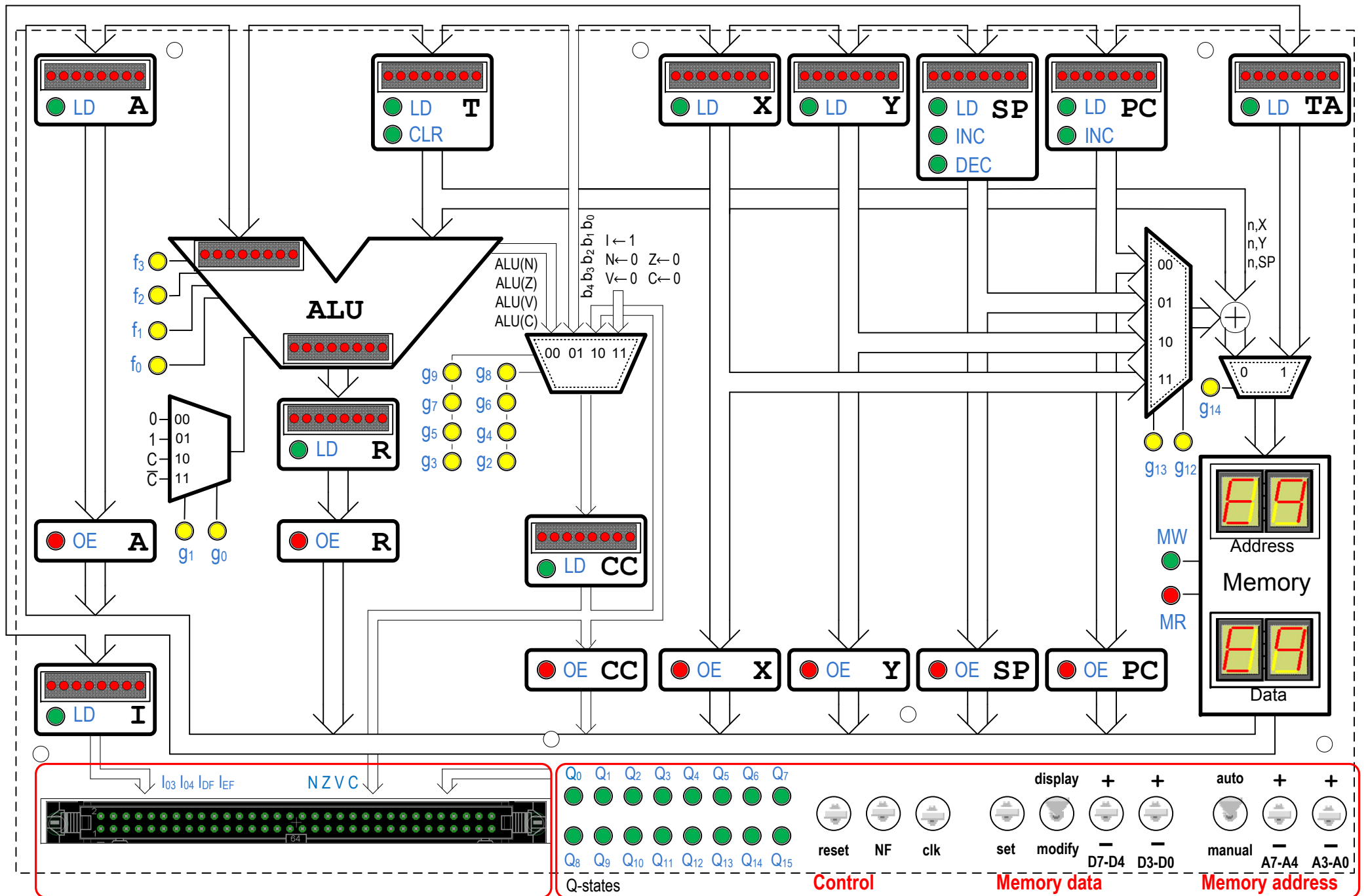
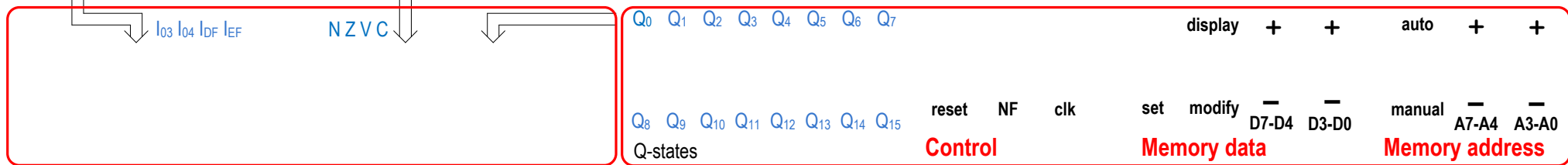
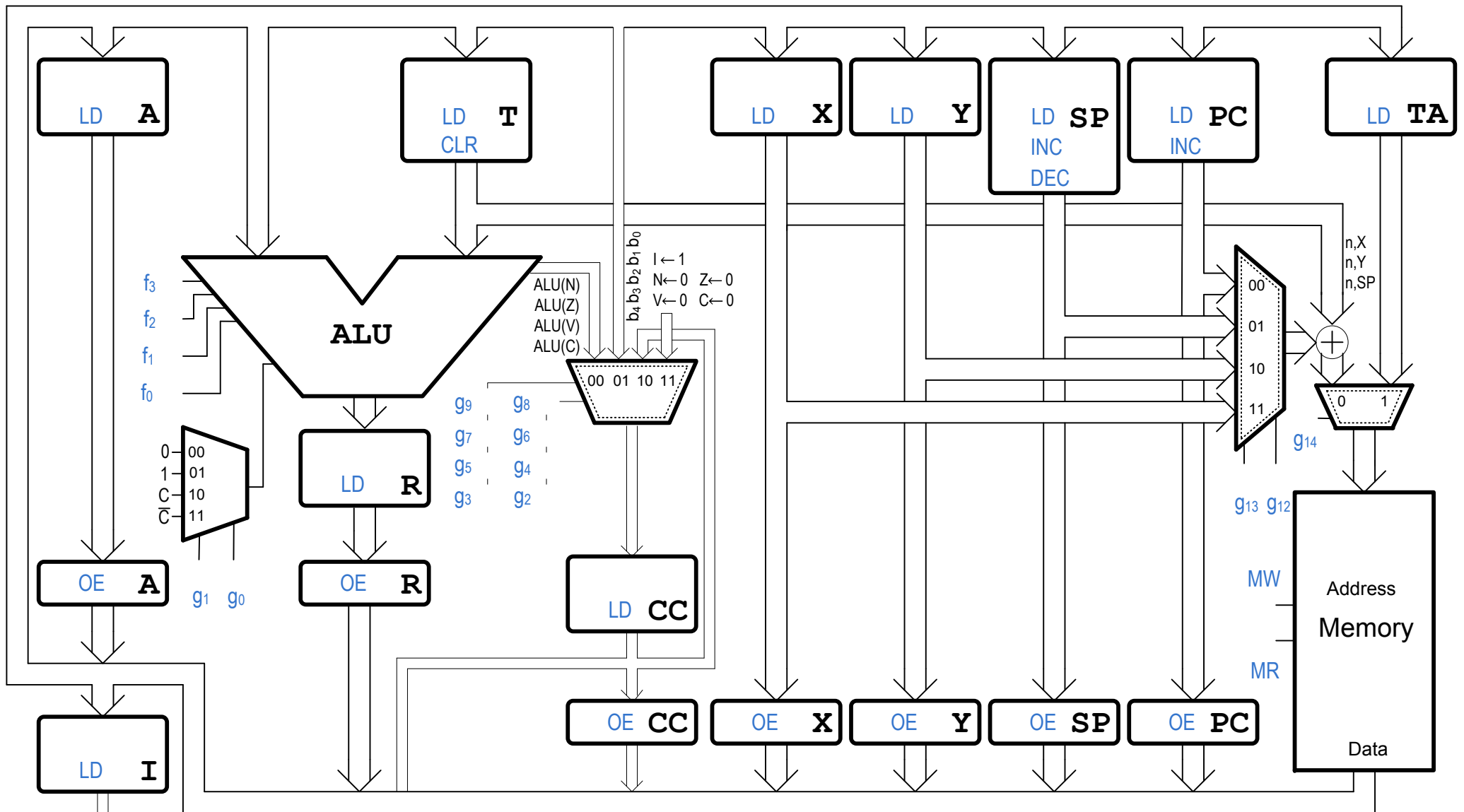
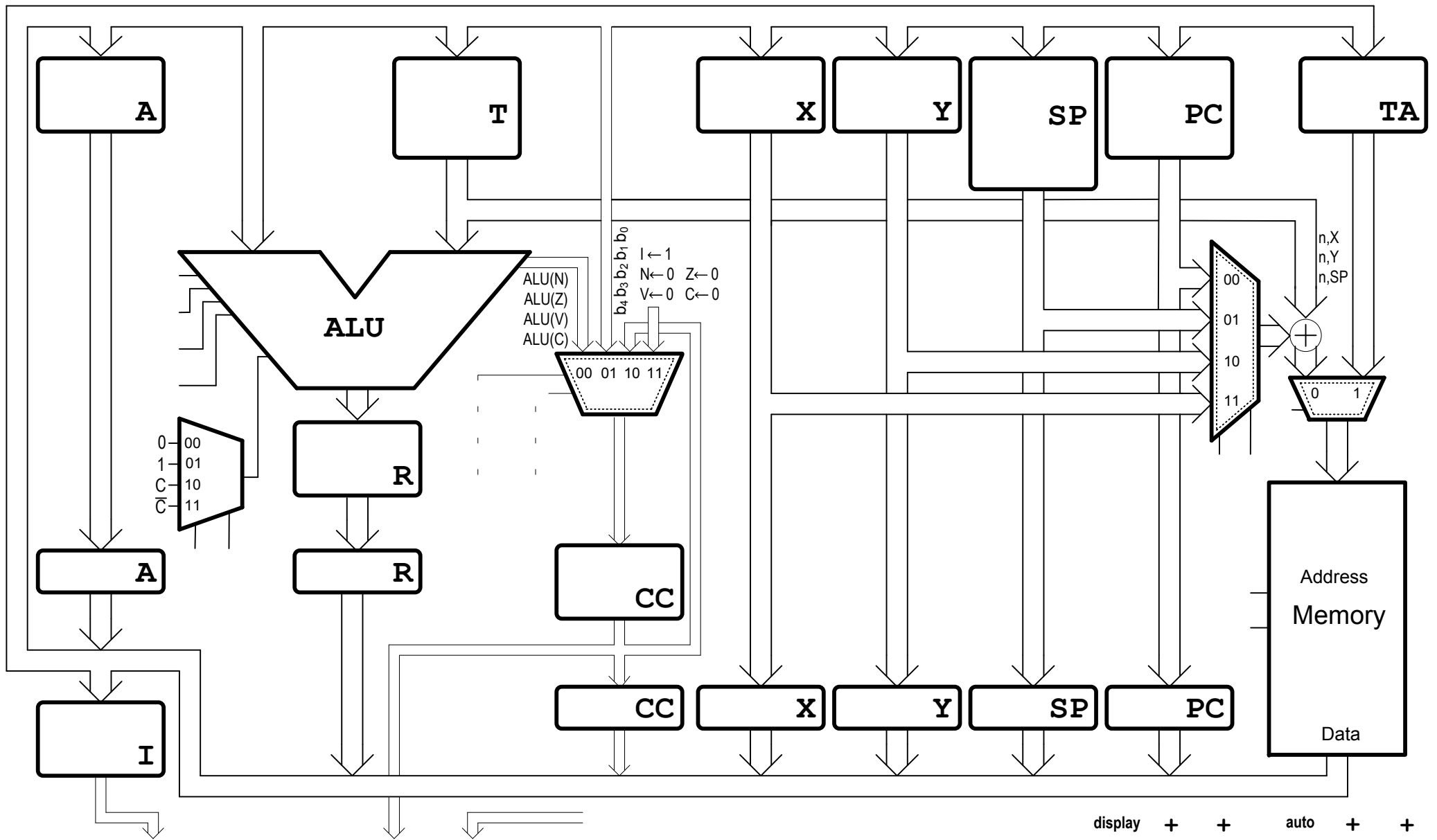








Q-states

LU3-FLISP-DV

LD

LD
CLR

LD

LD

LD
INC
DEC

LD
INC

LD

f₃
f₂
f₁
f₀

LD

g₉ g₈
g₇ g₆
g₅ g₄
g₃ g₂

g₁₄

g₁₃ g₁₂

OE

g₁ g₀

OE

LD

MW

MR

OE

OE

OE

OE

OE

LD

I₀₃ I₀₄ I_{DF} I_{EF}

N Z V C

Q₀ Q₁ Q₂ Q₃ Q₄ Q₅ Q₆ Q₇

Q₈ Q₉ Q₁₀ Q₁₁ Q₁₂ Q₁₃ Q₁₄ Q₁₅

Control

Memory data

Memory address

