Software Engineering using Formal Methods

Verification with SPIN

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Course Representatives

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Spin: Previous Lecture vs. This Lecture

Previous lecture

Spin appeared as a Promela simulator

This lecture

Intro to Spin as a model checker

What Does A Model Checker Do?

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MC's search for counter examples is exhaustive.

$\Rightarrow$ Finding no counter example proves stated correctness properties.

What does 'exhaustive search' mean here?

exhaustive search

=

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- explicit, local:
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  ```

- implicit, global:
  scheduling of concurrent processes
  (see next lecture)

Model Checker for This Course: SPIN

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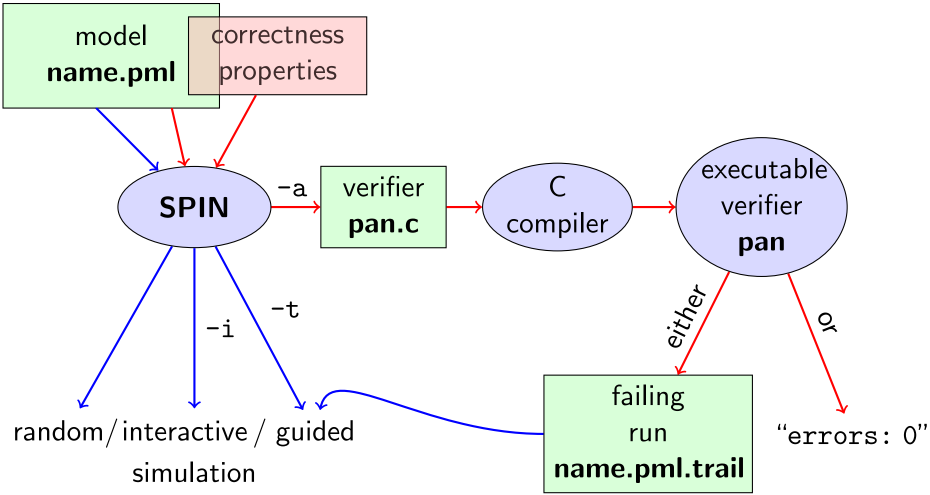
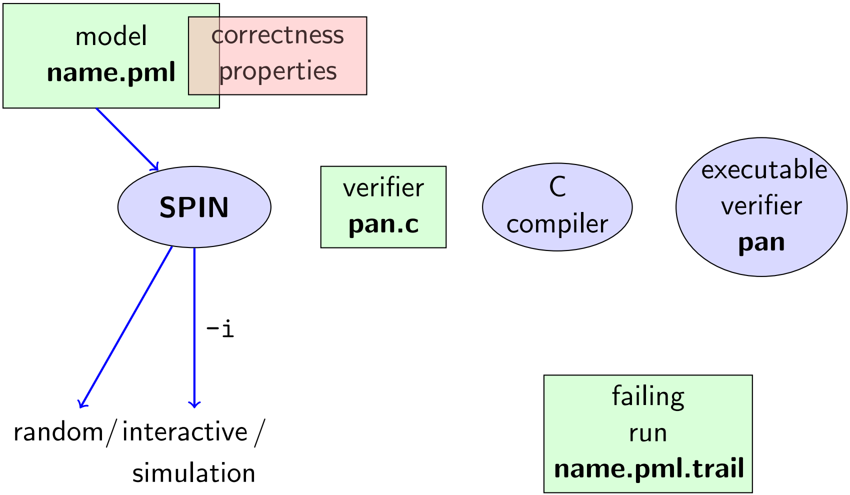
  generates a failing run of the model, to be simulated by Spin

SPIN Workflow: Overview



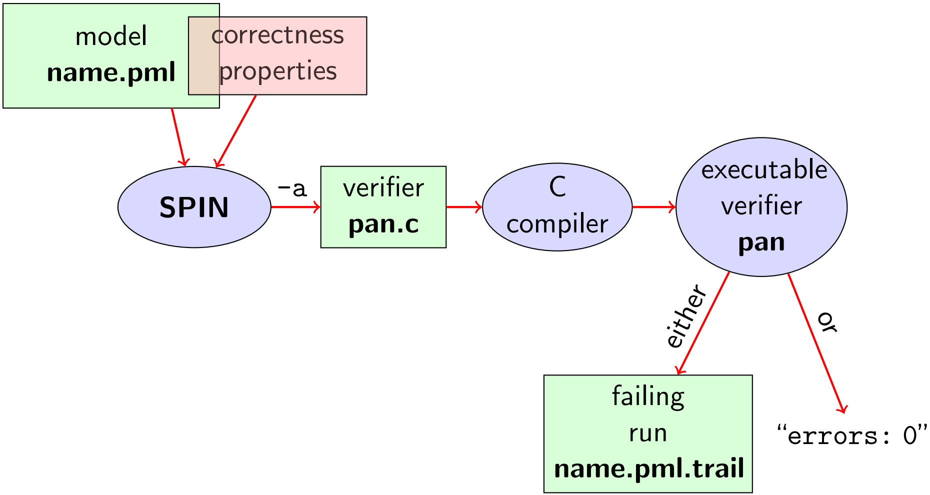
Plain Simulation with Spin



Rehearsal: Simulation Demo

- run example, random and interactive
  `zero.pml`

Model Checking with SPIN



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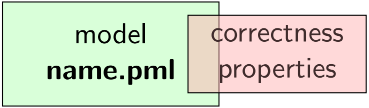
We know how to write models M.
But how to write Correctness Properties?

Stating Correctness Properties



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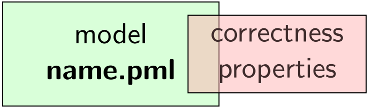

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model
name.pml

correctness
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stating properties within model, using

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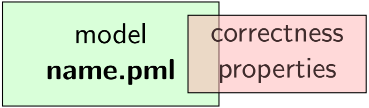

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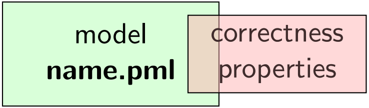

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stmt1;
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stmt1;
assert(max == a);
stmt2;
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```

```
...
if
  :: b1 -> stmt3;
           assert(x < y)
  :: b2 -> stmt4
...
```

Meaning of Boolean Assertion Statements

assert(*expr*)

- has no effect if *expr* evaluates to true
- triggers an error message if *expr* evaluates to false

This holds in both, simulation and model checking mode.

Meaning of General Assertion Statements

assert(*expr*)

- has no effect if *expr* evaluates to non-zero value
- triggers an error message if *expr* evaluates to 0

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Recall:

bool true false is syntactic sugar for

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$\Rightarrow$ general case covers Boolean case

Instead of using 'printf's for Debugging ...

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/* after choosing a,b from {1,2,3} */
if
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Command Line Execution

(simulate, inject fault, simulate again)

```
> spin [-i] max.pml
```

... we can employ Assertions

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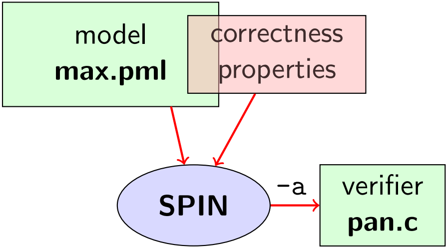
(Historic moment in the course.)

Generate Verifier in C



Command Line Execution

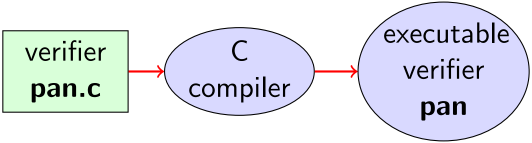
Generate Verifier in C

```
> spin -a max2.pml
```

SPIN generates Verifier in C, called **pan.c**

(plus helper files)

Compile To Executable Verifier



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> gcc -o pan pan.c
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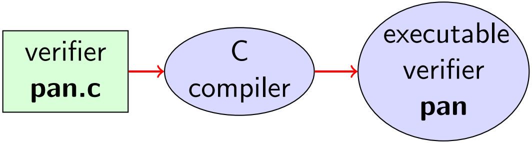

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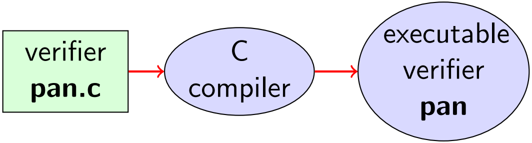

Command Line Execution

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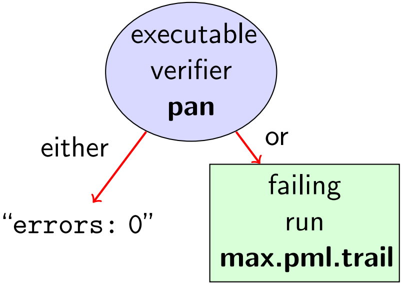
pan: historically "**p**rotocol **an**alyzer", now "**p**rocess **an**alyzer"

Run Verifier (= Model Check)



Command Line Execution

run verifier **pan**

> ./pan or *> pan*

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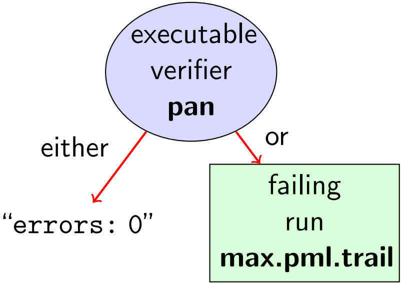

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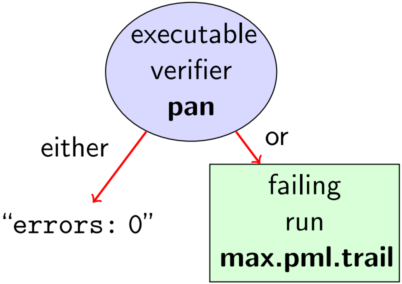

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`> ./pan` *or* `> pan`

- prints “`errors: 0`” $\Rightarrow$ Correctness Property verified!

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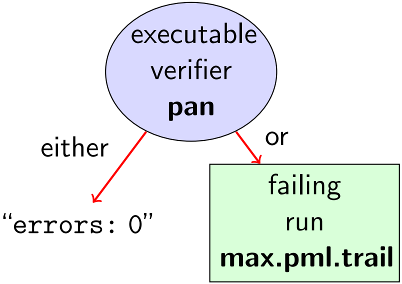

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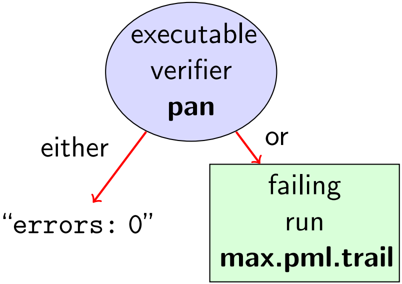

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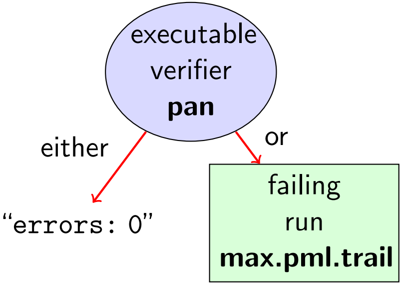

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- prints "`errors: 0`", or
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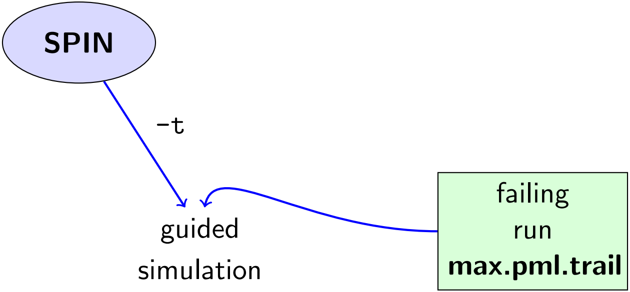
  records failing run in **max2.pml.trail**

Guided Simulation

To examine failing run: employ simulation mode, “guided” by trail file.



Command Line Execution

inject a fault, re-run verification, and then:

```
> spin -t -p -l max2.pml
```

Output of Guided Simulation

can look like:

```
Starting P with pid 0
1: proc 0 (P) line  8 "max2.pml" (state 1) [a = 1 ]
                P(0):a = 1
2: proc 0 (P) line 14 "max2.pml" (state 7) [b = 2 ]
                P(0):b = 2
3: proc 0 (P) line 23 "max2.pml" (state 13) [((a<=b))]
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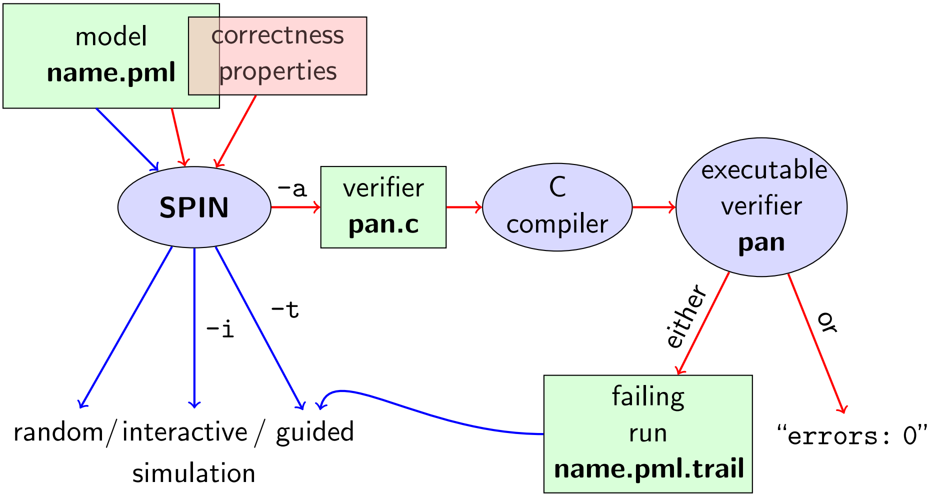
(If output doesn't mention `max` variable, re-verify with `./pan -E`)

What did we do so far?

following whole cycle (most primitive example, assertions only)



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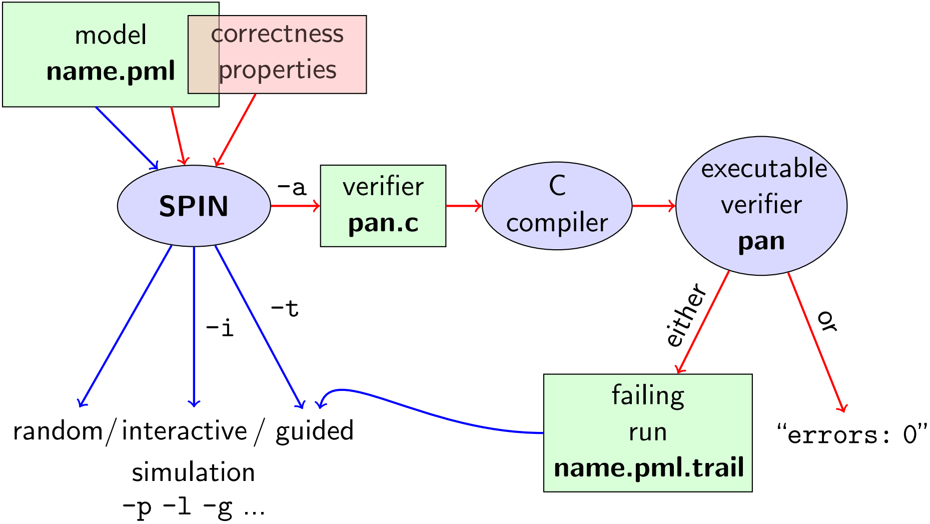

Further Examples: Integer Division

```
int dividend = 15;
int divisor  = 4;
int quotient, remainder;

quotient = 0;
remainder = dividend;
do
  :: remainder > divisor ->
     quotient++;
     remainder = remainder - divisor
  :: else ->
     break
od;
printf("%d␣divided␣by␣%d␣=␣%d,␣remainder␣=␣%d\n",
       dividend, divisor, quotient, remainder)
```

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int quotient, remainder;

quotient = 0;
remainder = dividend;
do
  :: remainder > divisor ->
     quotient++;
     remainder = remainder - divisor
  :: else ->
     break
od;
printf("%d␣divided␣by␣%d␣=␣%d,␣remainder␣=␣%d\n",
       dividend, divisor, quotient, remainder)
```

simulate, put assertions, verify, change values, ...

Further Examples: Greatest Common Divisor

greatest common divisor of x and y

```
int a, b;
a = x; b = y;
do
  :: a > b -> a = a - b
  :: b > a -> b = b - a
  :: a == b -> break
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printf("The␣GCD␣of␣%d␣and␣%d␣=␣%d\n", x, y, a)
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⇒ typical for model checking

Typical Command Lines

typical command line sequences:

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```
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```
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gcc -o pan pan.c
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```

and in case of error

```
spin -t -p -l -g name.pml
```

SPIN Reference Card

Ben-Ari produced Spin Reference Card, summarizing

- typical command line sequences
- options for
  - SPIN
  - gcc
  - pan
- PROMELA
  - datatypes
  - operators
  - statements
  - guarded commands
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⇒ available from course page (see ‘Links, Papers, and Software’)

Why SPIN?

- SPIN targets software, instead of hardware verification ("*Software* Engineering using Formal Methods")
- 2001 ACM Software Systems Award (other winning software systems include: Unix, TCP/IP, WWW, Tcl/Tk, Java)
- used for safety critical applications
- distributed freely as research tool, well-documented, actively maintained, large user-base in academia and in industry
- annual SPIN user workshops series held since 1995
- based on standard theory of (ω-)automata and linear temporal logic

Why SPIN? (Cont'd)

- PROMELA and SPIN are rather simple to use
- good to understand a few systems really well, rather than many systems poorly
- availability of good course book (Ben-Ari)
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- availability of front end JSPIN (also Ben-Ari)
- and now: availability of Bart's web interface

What is jSpin?

- graphical user interface for Spin
- developed for pedagogical purposes
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- makes back-end calls transparent

jSPIN Demo

Command Line Execution

calling jSPIN

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> java -jar /usr/local/jSpin/jSpin.jar
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(with path adjusted to your setting)

or use shell script:

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JSPIN Demo

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play around with similar examples ...

Catching A Different Type of Error

quoting from file **max3.pml**:

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/* after choosing a,b from {1,2,3} */
if
  :: a >= b -> max = a
  :: b <= a -> max = b
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generate and execute **pan**

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Note: no assert in **max3.pml**.

Catching A Different Type of Error

Further inspection of **pan** output:

```
...
pan: invalid end state  (at depth 1)
pan: wrote max3.pml.trail
...
```

Legal and Illegal Blocking

A process may legally block, as long as some other process can proceed.

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(Fix error)

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Example: `end.pml`

Can get SPIN to ignore 'invalid end state' error: `./pan -E`

Literature for this Lecture

Ben-Ari Chapter 2, Sections 4.7.1, 4.7.2