

## EVANGELOS VASILAKIS

Tunnlandsgatan 7, lgh 301  
Kaveros, Vastra Frolunda, 42138  
Gothenburg, Sweden  
evavas@chalmers.se  
evaggelos.vasilakis@gmail.com

### RESEARCH AREAS

Computer Architecture  
Memory Systems  
System, Processor Power/Energy Efficiency  
Energy Optimizations, Characterization and Modelling  
Hardware Design and Development  
ARM Processors and Architecture  
Security

### EDUCATION

#### Computer Science and Engineering:

- B.Sc Computer Science Department, University of Crete, November 2012
- M.Sc Computer Science Department, University of Crete, February 2015 (M.Sc Thesis: An Instruction Level Energy Characterization of ARM Processors)
- Ph.D Computer Science and Engineering Department, Chalmers University of Technology, May 2015 - Now, (Defence planned for summer 2020)

### WORK - RESEARCH EXPERIENCE

#### Graduate Research Assistant:

- Computer Architecture and VLSI Laboratory, Institute of Computer Science, Foundation of Research And Technology Hellas, 2012 - 2015

#### Teaching Assistant:

University of Crete:

- Digital Circuits Laboratory (Verilog HDL and FPGA Development), 2012
- Computer Organization, 2013 - 2014
- Computer Architecture, 2013 - 2014

Chalmers University of Technology:

- Parallel Computer Architecture, 2015 - 2019
- Digital Design, (VHDL and FPGA Development) 2016 - 2020

### SKILLS

#### Programming:

- C/C++
- Java
- X86 and ARM Assembly
- Bash Scripting
- PHP, HTML, SQL, ocaml
- Parallel Programming: OpenMP, Pthreads

#### Hardware Development:

- Verilog - VHDL
- Xiling Tools for FPGA Prototyping
- Basic Cadence and Synopsis tools for ASIC Design

**Misc:**

- Unix/Linux
- Latex
- Makefile
- SVN / Git Version Control Systems
- Simulators, Emulators - Models: Gem5, McPAT, DRAMSIM2

**MISC**

Citizenship: Greek

Date of Birth: 21st February 1985

Completed Greek Mandatory Army Service

**Languages:**

Greek:

- Native

English:

- Certificate of Proficiency in English, University of Cambridge
- Certificate of Proficiency in English, University of Michigan

**PUBLICATIONS**

**Evangelos Vasilakis**, Vassilis Papaefstathiou, Pedro Trancoso and Ioannis Sourdis.

**"Hybrid<sup>2</sup>: Combining Caching and Migration in Hybrid Memory Systems"**

2020 IEEE International Symposium on High Performance Computer Architecture, HPCA 2020

**Evangelos Vasilakis**, Vassilis Papaefstathiou, Pedro Trancoso and Ioannis Sourdis.

**"LLC-guided Data Migration in Hybrid Memory Systems"**

International Parallel and Distributed Processing Symposium, IPDPS 2019

**Evangelos Vasilakis**, Vassilis Papaefstathiou, Pedro Trancoso and Ioannis Sourdis.

**"Decoupled Fused Cache: Fusing a Decoupled LLC with a DRAM Cache"**

ACM Transactions on Architecture and Code Optimization, TACO January 2019

**Evangelos Vasilakis**, Vassilis Papaefstathiou, Pedro Trancoso and Ioannis Sourdis.

**"FusionCache: using LLC Tags for DRAM Cache"**

Design, Automation and Test in Europe, DATE 2018

Alirad Malek, **Evangelos Vasilakis**, Vassilis Papaefstathiou, Pedro Trancoso, Ioannis Sourdis.

**"Odd-ECC: On-demand DRAM Error Correcting Codes"**

4th Annual International Symposium on Memory Systems, MEMSYS 2017

**Evangelos Vasilakis**, Ioannis Sourdis, Vassilis Papaefstathiou, Antonis Psathakis and Manolis Katevenis.

**"Modeling Energy-Performance Tradeoffs in ARM big.LITTLE Architectures"**

Proc. of the 27th International Symposium on Power and Timing Modeling, PATMOS 2017

A. Psathakis, V. Papaefstathiou, N. Chrysos, F. Chaix, **E. Vasilakis**, D. Pnevmatikatos and M. Katevenis.

**"A systematic evaluation of emerging mesh-like CMP NoCs"**

2015 ACM/IEEE Symposium on Architectures for Networking and Communications Systems, ANCS 2015