## Chalmers | Göteborgs Universitet

# Concurrent Programming TDA384/DIT391 

Saturday, 26 October 2019
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(Exam set by K. V. S. Prasad, Sandro Stucki and Nir Pieterman, based on the course given Sep-Oct 2019)

## Material permitted during the exam (hjälpmedel):

Two textbooks; four sheets of A4 paper with notes; English dictionary.
Grading: You can score a maximum of 70 points. Exam grades are:

| points in exam | Grade Chalmers | Grade GU |
| ---: | ---: | ---: |
| $28-41$ | 3 | G |
| $42-55$ | 4 | G |
| $56-70$ | 5 | VG |

Passing the course requires passing the exam and passing the labs. The overall grade for the course is determined as follows:

| points in exam + labs | Grade Chalmers | Grade GU |
| ---: | ---: | ---: |
| $40-59$ | 3 | G |
| $60-79$ | 4 | G |
| $80-100$ | 5 | VG |

The exam results will be available in Ladok within 15 working days after the exam's date.

## Instructions and rules:

- Please write your answers clearly and legibly: unnecessarily complicated solutions will lose points, and answers that cannot be read will not receive any points!
- Justify your answers, and clearly state any assumptions that your solutions may depend on for correctness.
- Answer each question on a new page. Glance through the whole paper first; six questions, numbered Q1 through Q6. Do not spend more time on any question or part than justified by the points it carries.
- Be precise. In your answers, try to use the programming notation and syntax used in the questions. You can also use pseudo-code, provided the meaning is precise and clear. If need be, explain your notation.

Q1 (8p). Below is the pseudo-code of a program with two threads, p and q . The variables n and flag are shared between p and q .

| $\begin{gathered} \text { int } \mathrm{n}=0 ; \\ \text { boolean flag }=\text { false; } \end{gathered}$ |  |
| :---: | :---: |
| p | q |
| $\begin{array}{ll} \hline p_{1}: & \text { while }(\text { flag }==\text { false })\{ \\ p_{2}: & \mathrm{n}=1-\mathrm{n} ; \\ & \} \end{array}$ | ```\(q_{1}: \quad\) while(flag==false) \{ if( \(\mathrm{n}==0\) ) \{ flag \(=\) true \} \}``` |

The labels $p_{1}, p_{2}, q_{1}, q_{2}$ and $q_{3}$ are given only for ease of reference.
(Part a) Construct a scenario for which the program terminates.(2p)
(Part b) What are the possible values of n when the program terminates?
(Part c) Does the program terminate for all scenarios?
(Part d) Does the program terminate for all fair scenarios?

Q2 (18p). The pseudo-code below tries to solve the critical section (CS) problem with two threads, p and q . The keyword atomic encloses a pair of assignments that happen in one step, i.e., no instruction by the other thread can occur between those two assignments. Remember that CS must exit after a finite time, but NCS may loop.
The label $p_{i}$ can mean the command that follows $p_{i}$, or the proposition that thread p is at $p_{i}$, and the next command p will execute is $p_{i}$.

| int $\mathrm{tg}=1$; |  |  |  |
| :---: | :---: | :---: | :---: |
|  | p |  | q |
| $\begin{aligned} & \text { int } \mathrm{tp}=0 \\ & \text { while(true) }\{ \end{aligned}$ |  | int $\mathrm{tq}=0$; <br> while(true) \{ |  |
| $p_{1}$ | //NCS (non-critical section) do\{ | $q_{1}$ | //NCS (non-critical section) do\{ |
| $p_{2}$ : | atomic $\{\mathrm{tp}=\mathrm{tg} ; \mathrm{tg}=0\}$; | $q_{2}$ | $\begin{aligned} & \text { atomic }\{\mathrm{tq}=\mathrm{tg} ; \mathrm{tg}=0\} ; \\ & \} \text { while }(\mathrm{tq}!=1) ; \end{aligned}$ |
| $p_{3}$ : | \} while(tp! = 1); | $q_{3}$ |  |
| $p_{4}$ | //CS (critical section) | $q_{5}$ | //CS (critical section) |
| $p_{5}$ : | atomic $\{\mathrm{tp}=0 ; \mathrm{tg}=1\}$; |  | atomic $\{\mathrm{tq}=0 ; \mathrm{tg}=1\}$; |
|  |  |  |  |

A full state description would be a quintuple, ( $\left.p_{i}, q_{j}, \mathrm{tp}, \mathrm{tg}, \mathrm{tq}\right)$. For a refresher on logical notation, see Appendix B.
(Part a) Show that each of the three variables $\mathrm{tg}, \mathrm{tp}$, tq can take only the values 0 and 1 .
(Part b) Let $\mathrm{I}=\mathrm{tp}+\mathrm{tg}+\mathrm{tq}$. Prove $\mathrm{I}=1$ is invariant (always true).
Notation: Let tv stand for any one of $\mathrm{tp}, \mathrm{tg}$, tq .
Then from (Part a), $\neg(\mathrm{tv}=1)$ iff $\mathrm{tv}=0$.
From (Part b), in any state, exactly one of $\mathrm{tp}, \mathrm{tg}$, tq is 1 .
A state description need therefore only be a triple ( $p_{i}, q_{j}, \mathrm{tv}$ ), where tv is the one out of $\mathrm{tp}, \mathrm{tg}, \mathrm{tq}$ that has value 1 .
(Part c) Show that $p_{5} \rightarrow(t p=1)$.
(Part d) Show that $p_{2} \rightarrow((\operatorname{tp}=0) \wedge(\mathrm{tg}=1))$.
(Part e) Show that $\neg\left(p_{5} \wedge q_{5}\right)$ is an invariant, i.e., that the program guarantees mutual exclusion.
(Part f) Is there a loop of states from $\left(p_{2}, q_{2}, \mathrm{tg}\right)$ that includes neither $p_{5}$ nor $q_{5}$ ? I.e., can the system livelock, with p repeating $p_{2}$ and $p_{3}$, while q repeats $q_{2}$ and $q_{3}$ ?
(Part g) Thread q can starve thread $p$ if a certain command by $p$ never executes at a certain time. What command at what time? (3p)

Q3 (10p). The program from Q2 is repeated below for convenience. Read through Q2, including the clarifications and questions, before trying Q3.

| int $\mathrm{tg}=1$; |  |
| :---: | :---: |
| p | q |
| $\begin{aligned} & \text { int } \mathrm{tp}=0 \\ & \text { while(true) }\{ \end{aligned}$ | $\begin{aligned} & \text { int } \mathrm{tq}=0 \\ & \text { while(true) } \end{aligned}$ |
| $\begin{array}{ll} p_{1} & / / N C S \text { (non-critical section) } \\ & \operatorname{do}\{ \end{array}$ | $\begin{array}{ll} q_{1} & / / N C S \text { (non-critical section) } \\ & \operatorname{do}\{ \end{array}$ |
| $p_{2}: \quad$ atomic $\{\mathrm{tp}=\mathrm{tg} ; \mathrm{tg}=0\} ;$ | $q_{2}: \quad$ atomic $\{\mathrm{tq}=\mathrm{tg} ; \mathrm{tg}=0\} ;$ |
| $\left.p_{3}: \quad\right\}$ while $(\mathrm{t} \mathrm{p}!=1)$; | $\left.q_{3}: \quad\right\}$ while (tq $\left.!=1\right)$; |
| $p_{4} \quad / / C S$ (critical section) | $q_{4} \quad / / C S$ (critical section) |
| $p_{5}: \quad$ atomic $\{\mathrm{tp}=0 ; \mathrm{tg}=1\} ;$ | $q_{5}: \quad$ atomic $\{\mathrm{tq}=0 ; \mathrm{tg}=1\} ;$ |
| \} | $\}$ |

At the start, we expected each state to be a quintuple, $(\mathrm{tp}, \mathrm{tq}, \mathrm{tp}, \mathrm{tg}, \mathrm{tq})$ where $t p$ and $t q$ can take on 3 values each, and each of $t p, t g$, tq take on two values each, yielding 72 possible states. But Q2 tells us each state can be writen ( $\mathrm{tp}, \mathrm{tq}, \mathrm{tv}$ ), where each of tp , tq and tv can take 3 values, so we need only deal with 27 of those 72 program states.

Notation: We now also abbreviate our state notation by writing $p_{i} q_{j}$ to mean $\left(p_{i}, q_{j}, \mathrm{tg}\right), p_{i}^{\prime} q_{j}$ to mean $\left(p_{i}, q_{j}, \mathrm{tp}\right)$, and $p_{i} q_{j}^{\prime}$ to mean $\left(p_{i}, q_{j}, \mathrm{tq}\right)$. $\mathbf{Q}(2) \mathbf{b}$ tells us we won't need $p_{i}^{\prime} q_{j}^{\prime}$.

Here is a partial state transition table for the program above. You may assume that only 11 states are reachable from the start state $p_{2} q_{2}$.

| state $=p_{i} q_{j}$ perhaps primed |  | new state if p moves | new state if q moves |
| :--- | :---: | :---: | :---: |
| s1 | $p_{2} q_{2}$ | $p_{3}^{\prime} q_{2}=\mathrm{s} 8$ |  |
| s2 |  | $p_{3}^{\prime} q_{3}=\mathrm{s} 9$ |  |
| s3 | $p_{2} q_{3}^{\prime}$ |  | $p_{2} q_{5}^{\prime}=\mathrm{s} 4$ |
| s4 | $p_{2} q_{5}^{\prime}$ |  |  |
| s5 |  |  | $p_{3} q_{3}^{\prime}=\mathrm{s} 6$ |
| s6 | $p_{3} q_{3}^{\prime}$ |  |  |
| s7 | $p_{3} q_{5}^{\prime}$ |  |  |
| s8 | $p_{3}^{\prime} q_{2}$ |  |  |
| s9 | $p_{3}^{\prime} q_{3}$ |  |  |
| s10 | $p_{5}^{\prime} q_{2}$ |  |  |
| s11 | $p_{5}^{\prime} q_{3}$ |  |  |

(Part a) Fill in the blank entries in the table.
(Part b) Pick an unreachable state out of the 27 the abbreviated notation allows, and say why it is unreachable.

Q4 (12p). Consider the following event loop of a "compute server" in Erlang.

```
server_event_loop() ->
    receive
        {request, Fun, Args, From} ->
            spawn(
                fun() ->
                    Ans = apply(Fun, Args),
                    From ! {response, Ans}
                end)
    end,
server_event_loop().
```

(Part a). Give a sequence of messages sent by the server in response to the following sequence of requests sent from a single client.

```
Me = self(),
```

server ! \{request, fun exp/2, [10, 5], Me\},
server ! \{request, fun lists:append/2, [[1, 2], [3]], Me\},
server ! \{request, fun(X) -> $X+X$ end, [8], Me\},
where

```
exp(_, 0) -> 1;
exp(X, 1) -> X;
exp(X, Y) -> X * exp(X, Y - 1).
```

(Part b). Is your answer to Part a the only possible sequence that could have been observed?
(Part c). What is the point of spawning new (sub-)processes for each computation?
(Part d). Consider the following client code for sending a sequence of requests to the server and collecting responses
\%Send the messages to the server.
[server ! \{request, fun exp/2, [Num, Exp], self()\} || Num <- Nums],
3
4 \%Collect answers and print them.
5 Ans = [receive \{response, Ans\} -> Ans end || - <- Nums],
6 io:fwrite("~p ^ ~p = ~p~n", [Nums, Exp, Ans]).
Can this code go wrong? Will you get the expected output?
(Part e). Sketch a fix for this problem (no coding necessary, just suggest a fix in words).

Q5 (10p). The following recursive Java method add() implements vector addition for two arrays v1 and v2.

```
void add(int[] v1, int[] v2, int start, int end) {
    if (end - start < 1) return;
    else if (end - start == 1)
                            { v1[start] = v1[start] + v2[start]; }
    else {
            int mid = (start + end) / 2;
            add(v1, v2, start, mid);
            add(v1, v2, mid, end);
    }
```

10 \}

The method adds the entries of v1 and v2 from index start to index end -1 (excluding $v 1[e n d]$ and $v 2[e n d]$ ), and writes the result back into v 1 .

The class AddTask below implements a fork/join parallelization of add (). The constructor of AddTask has the same arguments as the method add(); the compute() method computes the result and stores it in the array v1. See Appendix A. 2 for a full code listing.

```
class AddTask extends RecursiveAction {
    private int[] v1, v2;
    private int start, end;
    AddTask(int[] v1, int[] v2, int start, int end) {
    this.v1 = v1; this.v2 = v2;
    this.start = start; this.end = end;
    }
    public void compute() {
        if (end - start < 1) return;
        else if (end - start == 1) {
            v1[start] = v1[start] + v2[start];}
        else {
            int mid = (start + end) / 2;
            AddTask t1 = new AddTask(v1, v2, start, mid);
            AddTask t2 = new AddTask(v1, v2, mid, end);
            t1.fork(); t2.fork();
            t1.join(); t2.join();
        }
    }
}
```

(Part a). Draw the dependency graph for a run of AddTask(v1, v2, 0, 8) assuming both v1 and v2 have exactly 8 elements. Each node should represent a task instance, with leaf nodes representing the base cases (where end-start $<=1$ ). Edges should represent parent-child relationships between tasks. How many nodes does the graph have? (2p)
(Part b). What is the approximate runtime of SumTask(arr, 0, 8) assuming there are at least 8 CPU cores and each task takes about one unit of time to perform its computation? How can this be inferred from the dependency graph?
(Part c). What is the maximum number of tasks that can be executed in parallel in this implementation (excluding parent tasks waiting for a child task to finish)? How can this be inferred from the dependency graph?
(Part d). From your dependency graph, deduce the total number of tasks started by AddTask when running an instance of AddTask (v1, v2, 0, 8). How does this compare to the maximum number of tasks running in parallel that you computed in $\operatorname{Part}(\mathbf{b})$ ? What does this difference correspond to?
(2p)
(Part e). Suppose an instance of AddTask(v1, v2, 0, 1000000) is run on a machine with a 4-core processor, and that tasks are scheduled using an optimally configured ForkJoinPool. Will this result in an efficient parallel computation? Name one source of inefficiency. (2p)

Q6 (14p). The code on page 9 is a modified sequential singly-linked list implementation. The list stores a sorted set of integers, and the method addIncrease adds elements in increasing order. We tried to convert it to a lock-free thread-safe implementation using Compare and Swap.
Besides the code shown on page 9, the interface of AtomicNode contains a getter and setter for val, and a getter for next.
In the List class on page 9 , notice that the list does not use the value field of the head node (lines 4, 11, and 18). This is intentional. The first element in the list is held in the next field of the head. The method addIncrease works as follows. The list is sorted in increasing order. So, if the next element in the list is larger than the value to add, then the value is added after the current element (lines $11-18$ ). If the next element in the list is the same as the value to add, then the search stops (and the value is not added again; lines 18-20). Otherwise, the method continues to scan the list (lines 21-23, and back to line 10). Finally, if there is no next element, the value is added at the end (lines 24-26 just after the while-loop (lines 10-23) terminates).
But there are bugs in this implementation with multiple threads: the list might not be sorted, or the same value may appear multiple times.
(Part a). Explain why the implementation has this issue.
(Part b). Update the code of addIncrease to fix the implementation. You should use a CAS (compare-and-set) synchronization primitive but no semaphores or locks. Use either Java or pseudo code.
You benchmark the list implementation versus the usage of the original sequential list protected by a lock. You create threads that use the new (thread-safe) list and run the following code:

```
for (int j = 0 ; j<100 ; ++j) {
    l.addIncrease(j);
```

3 \}

You create threads that use the old sequential implementation and one additional lock and run the following code:

```
for (int j=0 ; j<100 ; ++j) {
    lock.lock();
    l.addIncrease(j-1,j);
    lock.unlock();
}
```

(Part c). What happens when you run the thread-safe version vs the sequential version with the lock in the following scenarios: (i) there are 32 CPU cores and 32 threads, and (ii) there is 1 CPU core and 32 threads.

```
public class AtomicNode {
    private int val;
    private AtomicNode next;
    public boolean compareAndSetNext(AtomicNode existing,
                                    AtomicNode updated) {
    }
}
class List {
    AtomicNode head;
    public List() { // value of head not used
        head = new AtomicNode(0,null);
    }
    public void addIncrease(int val) {
        AtomicNode current = this.head;
        AtomicNode next = current.getNext();
        while (next != null) {
            if (next.getVal() > val) {
                AtomicNode temp;
                do {next = current.getNext();
                    temp = new AtomicNode(val,next);
                    } while (!current.compareAndSetNext(next,temp));
                return;
            }
            if (next.getVal() == val) {
                    return;
            }
            current = next;
            next = current.getNext();
        }
        do {next = current.getNext();
            temp = new AtomicNode(val,next);
        } while (!current.compareAndSetNext(next,temp));
    }
}
```

Figure 1: The interface AtomicNode snd the class List.

## A Full code listings

## A. 1 Code for Q4

```
module(q4_appendix).
-export([main/0]).
server_event_loop() -> % Server loop
    receive
        {request, Fun, Args, From} ->
            spawn(
                fun() ->
                Ans = apply(Fun, Args),
                    From ! {response, Ans}
            end)
    end,
    server_event_loop().
exp(_, 0) -> 1;
exp(X, 1) -> X;
exp(X, Y) -> X * exp(X, Y - 1).
main() -> % Start and register the server
    Pid = spawn(fun server_event_loop/0),
    register(server, Pid),
    Me = self(), % For Part a:
    server ! {request, fun exp/2, [10, 5], Me},
    server ! {request, fun lists:append/2, [[1, 2], [3]], Me},
    server ! {request, fun(X) -> X + X end, [8], Me},
    % Receive and print the responses.
    [ receive R -> io:fwrite("~p~n", [R]) end || _ <- [1, 2, 3]],
    Nums = [7,0,5,2], % For Part d:
    Exp = 3,
    % Send the messages to the server.
    [server ! {request, fun exp/2, [Num, Exp], self()} || Num <- Nums],
    % Collect answers and print them.
    Ans = [receive {response, Ans} -> Ans end || _ <- Nums],
    io:fwrite("~p ^ ~p = ~p~n", [Nums, Exp, Ans]).
```

```
A. 2 Code for Q5
import java.util.concurrent.*;
class AddTask extends RecursiveAction {
    private int[] v1, v2;
    private int start, end;
    AddTask(int[] v1, int[] v2, int start, int end) {
        this.v1 = v1; this.v2 = v2;
        this.start = start; this.end = end;
    }
    @Override public void compute() {
        if (end - start < 1) return;
        else if (end - start == 1) {
            v1[start] = v1[start] + v2[start];}
        else {
            int mid = (start + end) / 2;
            AddTask t1 = new AddTask(v1, v2, start, mid);
            AddTask t2 = new AddTask(v1, v2, mid, end);
            t1.fork(); t2.fork();
            t1.join(); t2.join();
        }
    }
}
    public static void main(String[] args) {
        final int N = 20;
        int[] numbers = new int[N];
        for (int i = 0; i < N; ++i) { numbers[i] = i; }
        AddTask m = new AddTask(numbers, numbers, 0, N);
        ForkJoinPool.commonPool().invoke(m);
        for (int i = 0; i < N; ++i) {
            System.out.println("numbers[" + i + "] = " + numbers[i]);
        }
    }
}
```


## B Linear Temporal Logic (LTL) notation

1. An atomic proposition such as $q_{2}$ (process $q$ is at label $q_{2}$ ) holds for a state $s$ if and only if process $q$ is at $q_{2}$ in $s$.
2. Let $\phi$ and $\psi$ be formulas of LTL. Formulas are either atomic propositions, or are built up from other formulas using the following operators: $\neg$ for "not", $\vee$ for "or", $\wedge$ for "and", $\rightarrow$ for "implies", $\square$ for "always", and $\diamond$ for "eventually". A convenient abbreviation is $\phi$ iff $\psi$ (i.e., $\phi$ if and only if $\psi)$ for $(\phi \rightarrow \psi) \wedge(\psi \rightarrow \phi)$.

These operators have the obvious meanings, but two differ from what might be your interpretation of the names. First, $\phi \vee \psi$ (" $\phi$ or $\psi$ ") is false iff both $\phi$ and $\psi$ are false. This is an "inclusive or", so $\phi \vee \psi$ is also true if both $\phi$ and $\psi$ are true. Second, $\phi \rightarrow \psi$ (" $\phi$ implies $\psi$ ") is false iff $\phi$ is true and $\psi$ is false. So, in particular, $\phi \rightarrow \psi$ is true if $\phi$ is false. The meanings of the operators $\square$ and $\diamond$ are defined below.
3. A path is a possible future of the system, a possibly infinite sequence of states, each reachable from the previous state in the path. A state $s$ satisfies formula $\phi$ if every path from $s$ satisfies $\phi$.
A path $\pi$ satisfies $\square \phi$ if $\phi$ holds for the first state of $\pi$, and for all subsequent states in $\pi$. The path $\pi$ satisfies $\diamond \phi$ if $\phi$ holds for some state in $\pi$.

Note thatand $\diamond$ are duals:

$$
\square \phi \equiv \neg \diamond \neg \phi \quad \text { and } \quad \diamond \phi \equiv \neg \square \neg \phi .
$$

