Formal Methods for Software Development Modeling Concurrency

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Main problem of concurrency: sharing computational resources

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```

Solutions to this must be carefully designed and verified, otherwise. . .

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Aim of $\operatorname{SPIN}\text{-style}$ model checking methodology:

exhibit

flaws in

software systems

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exhibit design flaws in

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Focus of this lecture:

Modeling and analyzing concurrent systems

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Focus of next lecture:

Modeling and analyzing distributed systems

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- ► lack of reproducability
 - ⇒ even if failures appear in test phase, often impossible to analyze/debug defect
- ▶ lack of time exhaustive testing exhausts the testers long before it exhausts behavior of the system...

Mission of Spin-style Model Checking

Offer an efficient methodology to

- ▶ improve the design
- exhibit defects

of concurrent and distributed systems

Activities in Spin-style Model Checking

- 1. model (critical aspects of) concurrent/distributed system with Prometa
- 2. state crucial properties with assertions, temporal logic, ...
- 3. use SPIN to check all possible runs of the model
- 4. analyze result, possibly re-work 1. and 2.

Activities in Spin-style Model Checking

- 1. model (critical aspects of) concurrent/distributed system with PROMELA
- 2. state crucial properties with assertions, temporal logic, ...
- 3. use Spin to check all possible runs of the model
- 4. analyze result, possibly re-work 1. and 2.

Separate concerns of model vs. property! Check the property you want the model to have, not the one it happens to have.

Main Challenges of Modeling

expressiveness

Model must be expressive enough to 'embrace' defects the real system could have

simplicity

Model must be simple enough to be 'model checkable', theoretically and practically

Modeling Concurrent Systems in Promela

In the $\ensuremath{\mathrm{SPIN}}$ approach, the cornerstone of modeling concurrent/distributed systems are

PROMELA processes.

Initializing Processes

Can be instantiated *implicitly* using 'active'.

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Can be instantiated implicitly using 'active'.

```
Can be instantiated explicitly with key word 'init'
init {
   printf("Hello_world\n")
}
```

init is used to start other processes with run statement.

Processes can be started explicitly using run

```
proctype P() {
   byte x;
   ...
}
init {
   run P();
   run P()
}
```

Each run operator starts copy of process (with copy of local variables)

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```

(Promela's run corresponds to Java's start, not to Java's run)

Atomic Start of Multiple Processes

By convention, $\operatorname{\mathbf{run}}$ operators enclosed in $\operatorname{\mathbf{atomic}}$ block

```
proctype P() {
   byte x;
   ...
}
init {
   atomic {
    run P();
    run P()
   }
}
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Effect: processes only start executing once all are created

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```

Effect: processes only start executing once all are created

(More on atomic later)

```
joining: waiting for all other processes to finish
byte result;
proctype P() {
init {
  atomic {
    run P();
    run P()
  (_nr_pr == 1); /*blocks until join*/
  printf("result = %d", result)
```

Joining Processes

```
joining: waiting for all other processes to finish
byte result;
proctype P() {
init {
  atomic {
     run P();
     run P()
   (_nr_pr == 1); /*blocks until join*/
   printf("result<sub>□</sub>=%d", result)
         built-in variable holding number of running processes
_nr_pr
_nr_pr == 1 only 'this' process (init) is running
```

Process Parameters

Processes may have formal parameters, instantiated by run:

```
proctype P(byte id; byte incr) {
    ...
}
init {
    run P(7, 10);
    run P(8, 15)
}
```

Active (Sets of) Processes

init can be made implicit by using the active modifier:

```
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   ...
}
```

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active proctype P() {
    ...
}
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active [n] proctype P() {
    ...
}
Implicit init will run n copies of P
```

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Local and Global Data

Variables declared outside of the processes are global to all processes.

Variables declared inside a process are local to that processes.

```
byte n;
proctype P(byte id; byte incr) {
   byte t;
   ...
}
n is global
t is local
```

Modeling with Global Data

Pragmatics of modeling with global data:

- **Shared memory** of concurrent systems often modeled by global variables of numeric (or array) type
- Status of shared resources (printer, traffic light, ...) often modeled by global variables of Boolean or enumeration type (bool/mtype).
- **Communication mediums** of distributed systems often modeled by global variables of channel type (chan). (next lecture)

Interference on Global Data

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("ProcuP, unu=u%d\n", n)
}
```

```
byte n = 0;
active proctype P() {
  n = 1;
  printf("ProcuP, unu=u%d\n", n)
}
active proctype Q() {
  n = 2;
  printf("ProcuQ, unu=u%d\n", n)
}
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byte n = 0;
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   n = 1;
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How many outputs possible?
```

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How many outputs possible?

Different processes can interfere on global data

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Examples

- interleave0.pml
 SPIN simulation, SPINSPIDER automata + transition system
- interleave1.pml
 SPIN simulation, adding assertion, fine-grained execution model,
 model checking
- 3. interleave5.pml SPIN simulation, SPIN model checking, trail inspection

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Executability addresses many issues in the interplay of processes.

Most synchronization primitives (test & set, compare & swap, semaphores, ...) can be modeled w. executability and atomicity.

Each statement has the notion of executability. Executability of basic statements:

statement type	executable
assignment	always
assertion	always
print statement	always
expression statement	
send/receive statement	

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Executability of compound statements:

if resp. do statement is executable iff any of its alternatives 1 is executable

An alternative is executable iff its guard (the first statement) is executable

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Executability of compound statements:

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if resp. do statement is executable iff any of its alternatives is executable
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An alternative is executable iff its guard (the first statement) is executable (Recall: in alternatives, "->" syntactic sugar for ";")

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(Inspect end.pml)

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Executability and Blocking

Definition (Blocking)

A statement blocks iff it is not executable.

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Executability, resp. blocking are the key to $\ensuremath{\mathrm{PROMELA}}\xspace$ -style modeling of solutions to synchronization problems.

Deadlock

Definition (Deadlock (simplified))

Let CRP be the set of currently running processes.

A deadlock is a point in the execution where

- CRP ≠ ∅
- ▶ all $p \in CRP$ are blocking

(Model check end.pml)

Valid End States

Definition (End Location)

End locations of a process P are:

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End locations of a process P are:

- P's textual end
- each location marked with an end label: "endxxx:"

Deadlock

Definition (Deadlock (full version))

Let CRP be the set of currently running processes.

Let $NEL \subseteq CRP$ be the set of (currently running) processes which are *not* at a valid end location.

A deadlock is a point in the execution where

- ightharpoonup NEL $\neq \emptyset$
- ▶ all $p \in NEL$ are blocking

Deadlock Detection

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(Fix end.pml)

Atomicity

limit the possibility of sequences being interrupted by other processes

weakly atomic sequence

can only be interrupted when a statement blocks

strongly atomic sequence

cannot be interrupted at all

Atomicity

limit the possibility of sequences being interrupted by other processes

weakly atomic sequence

can only be interrupted when a statement blocks defined in PROMELA by atomic{list_of_statements}

strongly atomic sequence

cannot be interrupted at all defined in Prometa by d_step{list_of_statements}

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Executability (Cont'd)

 $\begin{array}{c} \mathbf{atomic} \ \mathsf{resp.} \ \mathbf{d_step} \ \mathsf{statement} \ \mathsf{is} \ \mathsf{executable} \\ \mathsf{iff} \\ \mathsf{guard} \ \mathsf{(i.e., the first inner statement)} \ \mathsf{is} \ \mathsf{executable} \end{array}$

Deterministic Sequences

$d_step:$

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Deterministic Sequences

d_{step} :

- strongly atomic
- deterministic (like a single step)
- choices resolved in fixed way (always take the first possible option)
 ⇒ avoid choices in d_step
- ▶ it is an error if any statement within d_step, other than the first one (called 'guard'), blocks

```
\begin{array}{ll} d\_step & \{ \\ & \texttt{stmt1}; \; \leftarrow \; \textit{guard} \\ & \texttt{stmt2}; \\ & \texttt{stmt3} \\ \} \end{array}
```

If stmt1 blocks, d_step is not entered, and blocks as a whole.

It is an error if stmt2 or stmt3 block.

(Weakly) Atomic Sequences

atomic:

- weakly atomic
- can be non-deterministic

```
atomic {
    stmt1; ← guard
    stmt2;
    stmt3
}
```

If guard blocks, atomic is not entered, and blocks as a whole.

Once **atomic** is entered, control is kept until a statement blocks, and only in this case passed to another process.

 $\label{lem:concurrent} Archetypal\ problem\ of\ concurrent\ systems$

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Critical section: Section of code/model where interference of other processes can cause problems

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Mutual Exclusion At most one process is executing its critical section at any time.

Archetypal problem of concurrent systems

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Archetypal problem of concurrent systems

Critical section: Section of code/model where interference of other processes can cause problems

Given a number of looping processes, each containing a critical section, design an algorithm such that:

- **Mutual Exclusion** At most one process is executing its critical section at any time.
- **Absence of Deadlock** If *some* processes are trying to enter their critical sections, then *one* of them must eventually succeed.
- **Absence of (individual) Starvation** If *any* process tries to enter its critical section, then *that* process must eventually succeed.

For demonstration and simplicity:

Noncritical and critical sections only **printf** statements here

```
active proctype P() {
 do :: printf("P_non-critical_actions\n");
        /* begin critical section */
        printf("Pusesushareduresourses\n")
        /* end critical section */
 od
active proctype Q() {
 do :: printf("Qunon-criticaluactions\n");
        /* begin critical section */
        printf("Quusesushareduresourses\n")
        /* end critical section */
 od
```

```
More infrastructure to achieve ME.
Adding two Boolean flags:
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("P<sub>||</sub>non-critical<sub>||</sub>actions\n");
          P in CS = true:
          /* begin critical section */
          printf("P<sub>|</sub>uses<sub>|</sub>shared<sub>|</sub>resourses\n");
          /* end critical section */
          P in CS = false
  od
active proctype Q() {
  \dots correspondingly \dots
```

```
adding assertions
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
        P_{in}CS = true;
        /* begin critical section */
        printf("P_uses_shared_resourses\n");
        assert(!Q_in_CS);
        /* end critical section */
        P_{in}_{CS} = false
  od
active proctype Q() {
    .....assert(!P_in_CS);......
}
```

```
bool P_in_CS = false;
bool Q_in_CS = false;
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
        P_{in}_{CS} = true;
        do :: !Q_in_CS -> break
            :: else -> skip
        od;
        /* begin critical section */
        printf("P_uses_shared_resourses\n");
        assert(!Q_in_CS);
        /* end critical section */
        P_{in}CS = false
  od
active proctype Q() { ...correspondingly... }
```

Mutual Exclusion by Blocking

Instead of Busy Waiting, process should

- 1. yield control,
- 2. continue to run only when exclusion properties becomes true again.

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What can we do instead?

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- 1. yield control,
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What can we do instead?

We use expression statement !Q_in_CS to let process P block where it should not proceed!

```
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
         P_{in}_{CS} = true;
          !Q_in_CS:
          /* begin critical section */
          printf("P<sub>□</sub>uses<sub>□</sub>shared<sub>□</sub>resourses\n");
          assert(!Q_in_CS);
          /* end critical section */
          P in CS = false
  od
active proctype Q() {
  \dots correspondingly \dots
```

Verify with $\ensuremath{\mathrm{S}}\xspace\mathrm{PIN}$

Verify with SPIN

 SPIN error (invalid end state)

 \Rightarrow deadlock

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can make pan ignore the deadlock: ./pan -E

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Spin still reports assertion violation(!)

In this example:

▶ mutual exclusion (ME) cannot be shown by SPIN

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- ▶ P/Q_in_CS not sufficient for proving ME

In this example:

- ▶ mutual exclusion (ME) cannot be shown by SPIN
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- ▶ P/Q_in_CS not sufficient for proving ME

Need more infrastructure.

Ghost variables: variables for verification, not for modeling

Show Mutual Exclusion with Ghost Variable

```
int critical = 0;
active proctype P() {
  do :: printf("Punon-criticaluactions\n");
         P_{in}_{CS} = true;
         ! Q in CS:
         /* begin critical section */
         critical++:
         printf("P_uses_shared_resourses\n");
         assert(critical < 2);</pre>
         critical--:
         /* end critical section */
         P_{in}CS = false
  od
active proctype Q() {
  \dots correspondingly \dots
```

SPIN (./pan -E) shows no assertion is violated \Rightarrow mutual exclusion is verified

```
{
m SPIN} (./pan -E) shows no assertion is violated \Rightarrow mutual exclusion is verified
```

Still SPIN (without -E) reports (invalid end state) \Rightarrow deadlock

Deadlock Hunting

Invalid End State:

- A process does not finish at its end
- ► OK if it is not crucial to continue add end lables (see end.pml)
- If it is crucial to continue:
 Real deadlock

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 Real deadlock

Address Deadlock with SPIN:

- ▶ Verify to produce a failing run trail
- Simulate to see how the processes get to the interlock
- ► Fix the model (not using the end labels nor -E option)

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Atomicity against Deadlocks

solution:

checking and setting the flag in one atomic step

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(demonstrate that in csGhost.pml)

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checking and setting the flag in one atomic step

```
(demonstrate that in csGhost.pml)
    atomic {
       !Q_in_CS;
       P_in_CS = true
}
```

► Verification artifacts:

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 - temporal logic (later in the course)
- ► Max *n* processes allowed in critical section modeling possibilities include:
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 - semaphores (see demo)
- More fine grained exclusion conditions, e.g.
 - several critical sections (Leidestraat in Amsterdam)

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Using atomic and d_step too heavily, for too large blocks, can result in well-behaved models, while modeling the wrong system.