

## Lecture 2

### Multistep evaluation relation

This lecture closely followed *Software foundations*, Vol. 2, on Small Step Operational Semantics.

Given a binary relation  $\rightarrow$  we define its reflexive transitive closure by the rules

$$\frac{}{e \rightarrow^* e} \quad \frac{e \rightarrow e_1 \quad e_1 \rightarrow^* e_2}{e \rightarrow^* e_2}$$

**Theorem 0.1**  $\rightarrow^*$  is reflexive, transitive and contains  $\rightarrow$ . It is the least reflexive transitive relation which contains  $\rightarrow$ .

The next result was already in Frege's 1879 book (which introduced quantifiers and proof system for higher order logic).

**Theorem 0.2** If  $\rightarrow$  is deterministic and  $e \rightarrow^* e_1$  and  $e \rightarrow^* e_2$  then  $e_1 \rightarrow^* e_2 \vee e_2 \rightarrow^* e_1$

We define  $NF(e, e_1)$  to mean  $e \rightarrow^* e_1$  and  $\neg \exists e' (e_1 \rightarrow e')$

**Theorem 0.3** If  $\rightarrow$  is deterministic and  $NF(e, e_1)$  and  $NF(e, e_2)$  then  $e_1 = e_2$

Intuitively  $NF(e, e_1)$  means that  $e_1$  is the result of the computation of  $e$ . The Theorem states that if the one step evaluation relation is deterministic then the result of the computation is uniquely determined (if it exists).

In the particular case of arithmetic expressions

$$e ::= v \mid \text{add } e \ e \quad v ::= \text{const } n$$

where

$$n ::= 0 \mid \text{succ } n$$

We can define the value as a function from expressions to natural numbers

$$\llbracket \text{const } n \rrbracket = n \quad \llbracket \text{add } e_0 \ e_1 \rrbracket = \llbracket e_0 \rrbracket + \llbracket e_1 \rrbracket$$

We have described leftmost evaluation by the rules

$$\frac{}{\text{add } (\text{const } n_0) \ (\text{const } n_1) \rightarrow \text{const } (n_0 + n_1)} (C)$$

$$\frac{e_0 \rightarrow e'_0}{\text{add } e_0 \ e_1 \rightarrow \text{add } e'_0 \ e_1} (A_0) \quad \frac{e_1 \rightarrow e'_1}{\text{add } (\text{const } n) \ e_1 \rightarrow \text{add } (\text{const } n) \ e'_1} (A_1)$$

We have seen that this is a deterministic evaluation relation. In this case, the evaluation of any expression terminates.

**Theorem 0.4** We have  $\forall e \exists e_1 NF(e, e_1)$ . Actually  $\forall e NF(e, \text{const}(\llbracket e \rrbracket))$ .

The proof is by induction on  $e$ , using the following.

**Lemma 0.5** If  $e \rightarrow^* e'$  then  $\text{add } e \ e_1 \rightarrow^* \text{add } e' \ e_1$  and  $\text{add } v \ e \rightarrow^* \text{add } v \ e'$ .

We can define in a similar way Boolean expressions

$$e ::= v \mid \text{if } e \ e \ e \quad v ::= \text{const } b$$

where

$$b ::= \text{true} \mid \text{false}$$

and the evaluation rules are

$$\frac{}{\text{if } (\text{const true}) \ e_0 \ e_1 \rightarrow e_0} \quad \frac{}{\text{if } (\text{const false}) \ e_0 \ e_1 \rightarrow e_1}$$

$$\frac{e \rightarrow e'}{\text{if } e \ e_0 \ e_1 \rightarrow \text{if } e' \ e_0 \ e_1}$$

**Theorem 0.6** The relation  $\rightarrow$  is deterministic and we have  $\forall e \exists e' NF(e, e')$ .

## A simple abstract machine and compiler correctness proof

What we present is a simplified version of the fundamental paper of McCarthy and Painter on correctness of a compiler for arithmetic expressions (1967).

We define the instruction list (code) as

$$cd ::= \text{LOAD } n \ cd \mid \text{ADD } cd \mid \text{HALT}$$

and the compilation function is

$$\text{comp } (\text{const } n) \ cd = \text{LOAD } n \ cd \quad \text{comp } (\text{add } e_0 \ e_1) \ cd = \text{comp } e_1 \ (\text{comp } e_0 \ (\text{ADD } cd))$$

The machine has then for state a pair  $cd, S$  where  $cd$  is a code and  $S$  is a stack of numbers. The small step semantics for this machine is

$$\frac{}{\text{ADD } cd, \ n_1 : n_0 : S \rightarrow cd, (n_1 + n_0) : S} \quad \frac{}{\text{LOAD } n \ cd, S \rightarrow cd, n : S}$$

We can now state, and prove by induction on  $e$

**Theorem 0.7** For all expression  $e$  we have  $\forall cd \forall S \quad \text{comp } e \ cd, S \rightarrow^* cd, \llbracket e \rrbracket : S$