## Some abstract machines

## Arithmetic expression

What we present is a very simplified version of the fundamental paper of McCarthy and Painter on correctness of a compiler for arithmetic expressions (1967). The expressions are

 $e ::= \operatorname{const} n \mid \operatorname{add} e e$ 

and the semantics is

[[const n]] = n  $[[\text{add } e_0 \ e_1]] = [[e_0]] + [[e_1]]$ 

We define the instruction list (code) as

$$cd ::= LOAD n cd | ADD cd | HALT$$

and the compilation function is

 $comp \ (const \ n) \ cd = LOAD \ n \ cd$   $comp \ (add \ e_0 \ e_1) \ cd = comp \ e_1 \ (comp \ e_0 \ (ADD \ cd))$ 

The machine has then for state a pair cd, S where cd is a code and S is a stack of numbers. The small step semantics for this machine is

 $\overline{\mathsf{ADD}\ cd,\ n_1:n_0:S\mapsto cd,(n_1+n_0):S}\qquad\overline{\mathsf{LOAD}\ n\ cd,S\mapsto cd,n:S}$ 

We can now state, and prove by induction on e

**Theorem 0.1** For all expression e we have  $\forall cd \ S$  comp  $e \ cd, S \mapsto^* cd, \llbracket e \rrbracket : S$ 

## Krivine Abstract Machine

We define the *terms* (in de Bruijn notation) as

$$t ::= n \mid \lambda t \mid t t$$

namely deBruijn index, or abstraction, or application.

A value u is a pair  $t\rho$  of a term and an environment, where an *environment*  $\rho$  is a list of values.

Krivine Abstract Machine has for states  $t \mid \rho \mid S$  where  $t\rho$  is a value and S is a stack of values. The small step semantics is

$$\overline{0 \mid (t\rho,\nu) \mid S \mapsto t \mid \rho \mid S} \qquad \overline{n+1 \mid (u,\nu) \mid S \mapsto n \mid \nu \mid S}$$
$$\overline{\lambda t \mid \rho \mid u : S \mapsto t \mid (u,\rho) \mid S}$$
$$\overline{t_0 \ t_1 \mid \rho \mid S \mapsto t_0 \mid \rho \mid (t_1\rho) : S}$$

So abstraction is "pop" while application is "push".