
Introduction to verifying concurrent systems using SPIN

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What is SPIN?

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- The SPIN model checker is a software tool for specifying and verifying concurrent and distributed systems
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 - It uses the programming language **Promela** to write the models
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What is SPIN?

- SPIN looks for counterexamples showing that the program breaks the spec.
 - no counterexample = correct!
 - If the state-space is too large, it will not terminate (timeout)
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SPIN's relation to this course?

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- Write programs from the book in Promela
 - Check for mutex, deadlocks and liveness...
 - We use assertions or LTL formulas
 - assert a safety property at a point in the program:
assert(count == n)
 - *What if the program never gets there?*
 - LTL formulas express properties about future states
 - (e.g., absence of deadlock, and liveness)
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SPIN's relation to this course? 2

- Not required for the exam
 - But may help to make proofs more concrete
 - Helps understand formal logic
 - SPIN can confirm your informal proofs
 - Help you understand when a “proof” really is a proof and when it is handwaving
 - Use it for exercises in the book!
 - Formal logic is a small part of the exam ($\frac{1}{6}$)
 - SPIN helps you practice with this
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How to use SPIN

- The official SPIN webpage:
<http://spinroot.com/spin/whatispin.html>
 - The web interface for SPIN by Bart van Delft
webpage: <http://abu.se.informatik.tu-darmstadt.de/sefm/spin/>
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